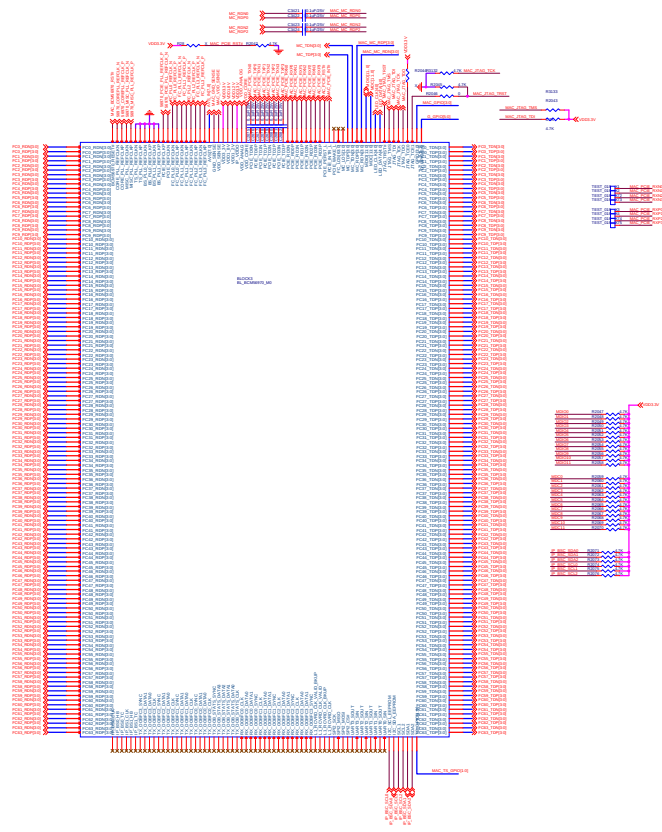
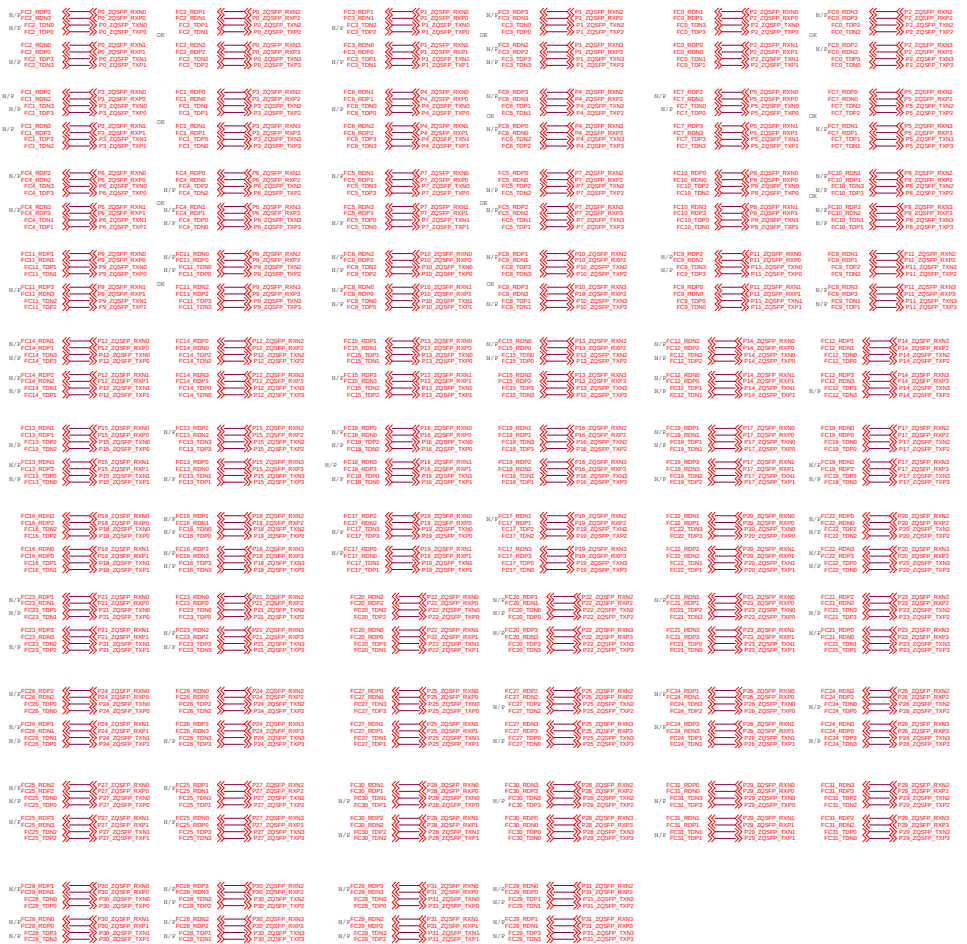
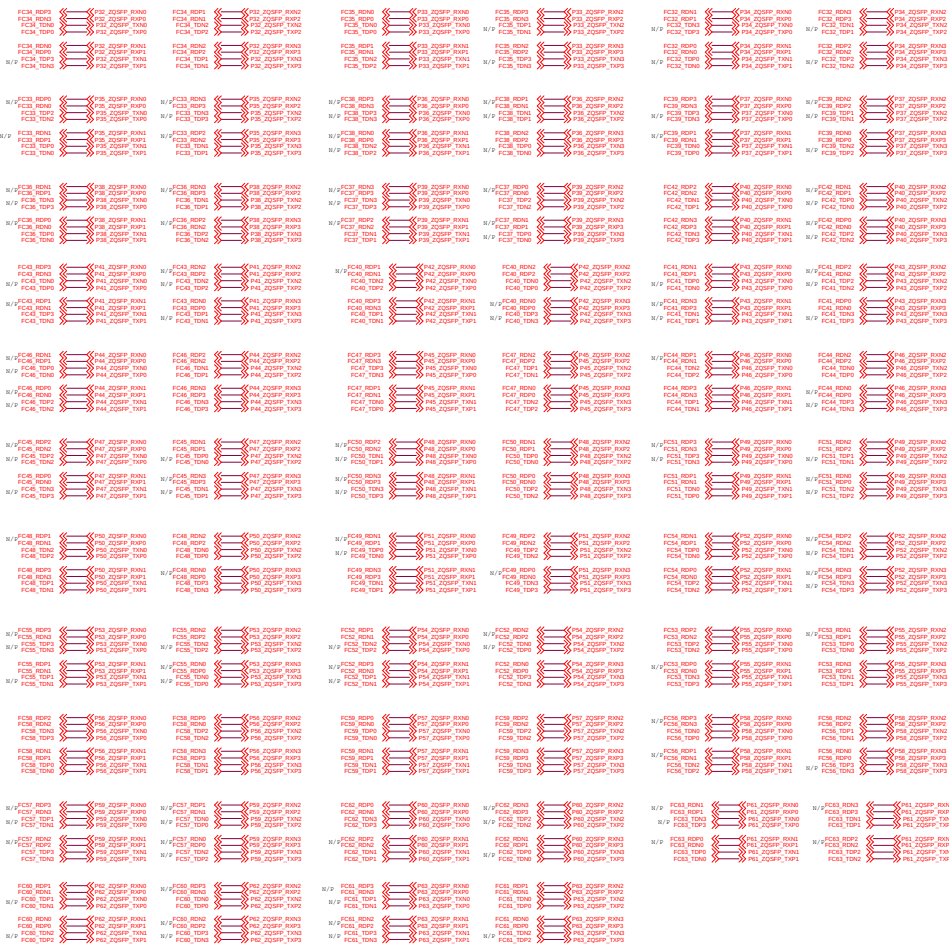
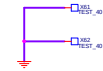
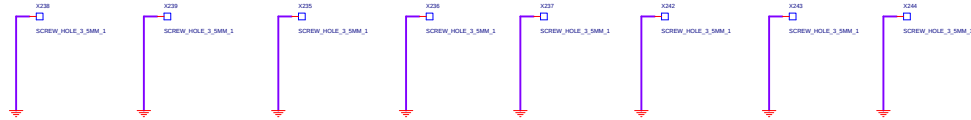
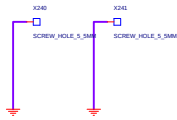
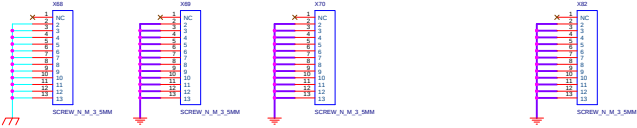
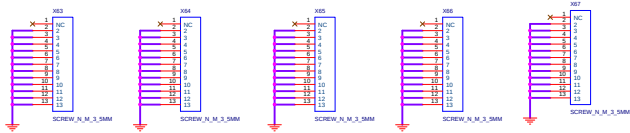
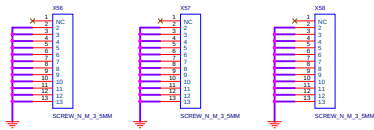
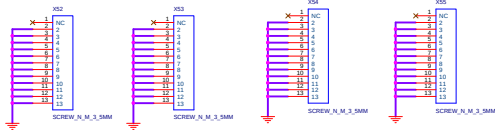
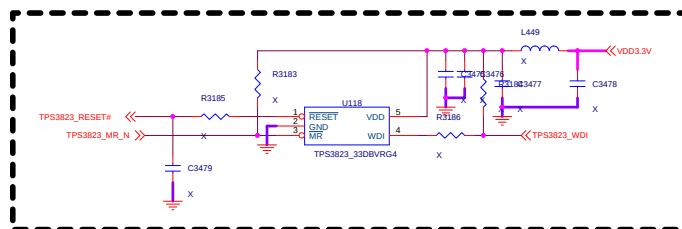
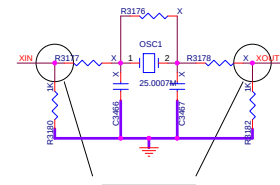


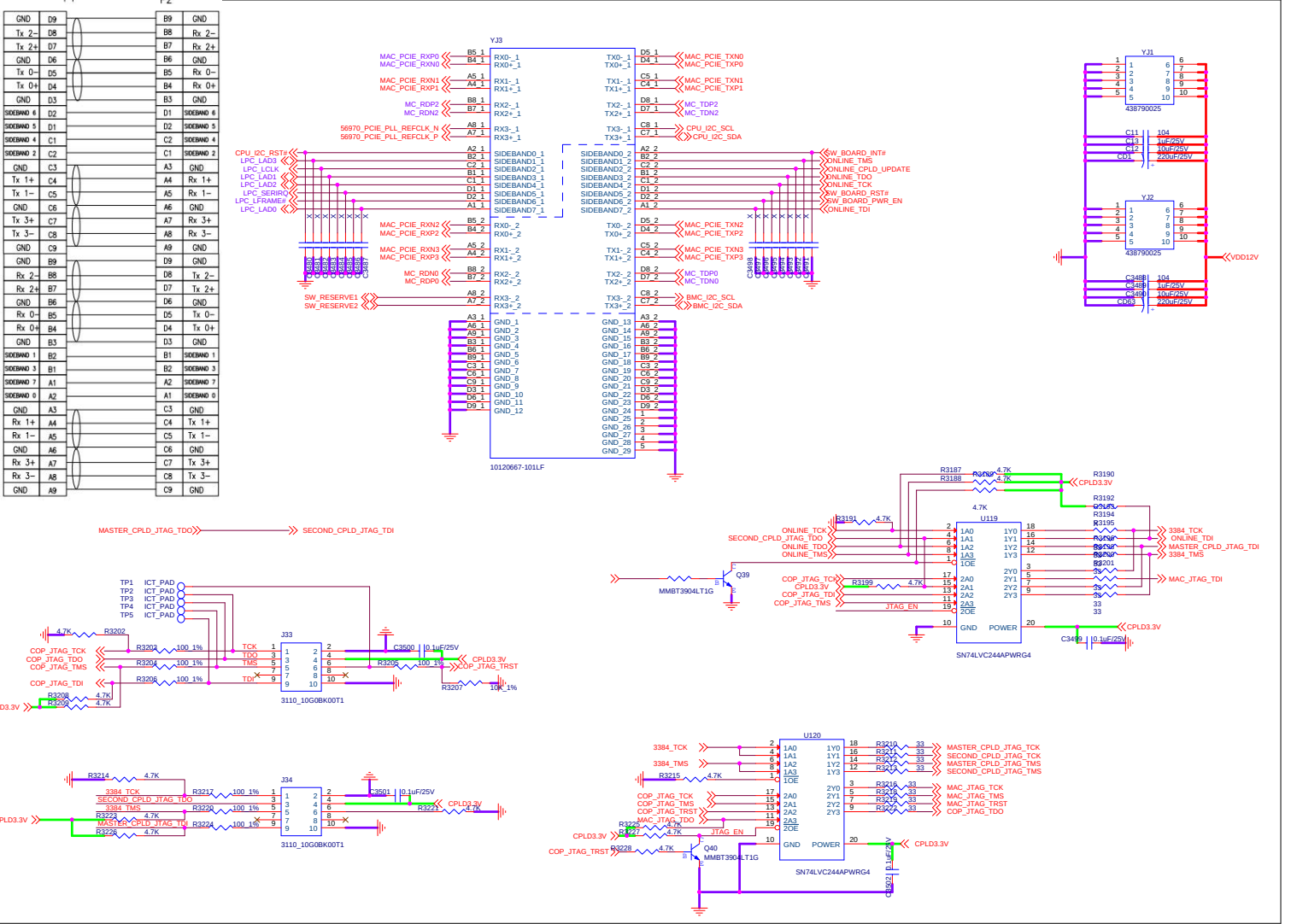


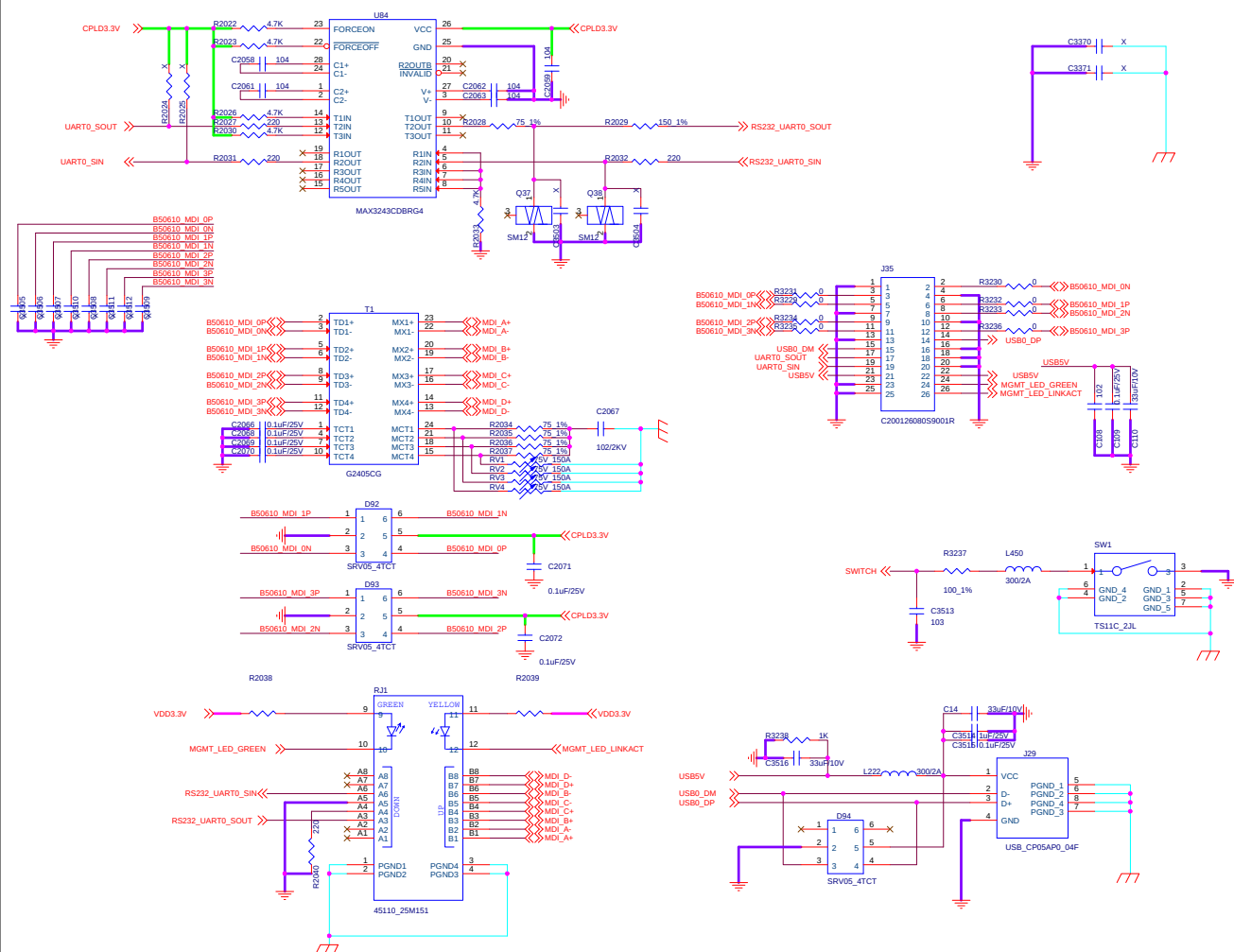


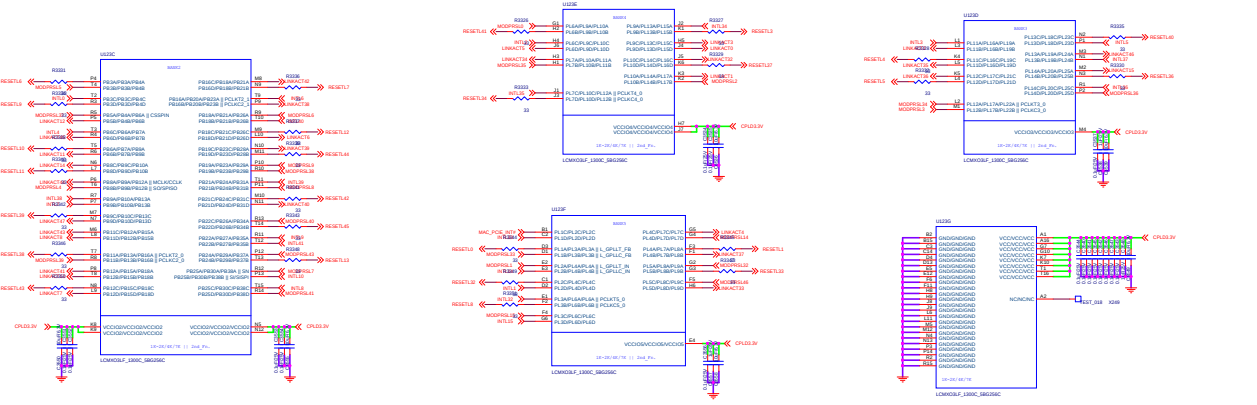
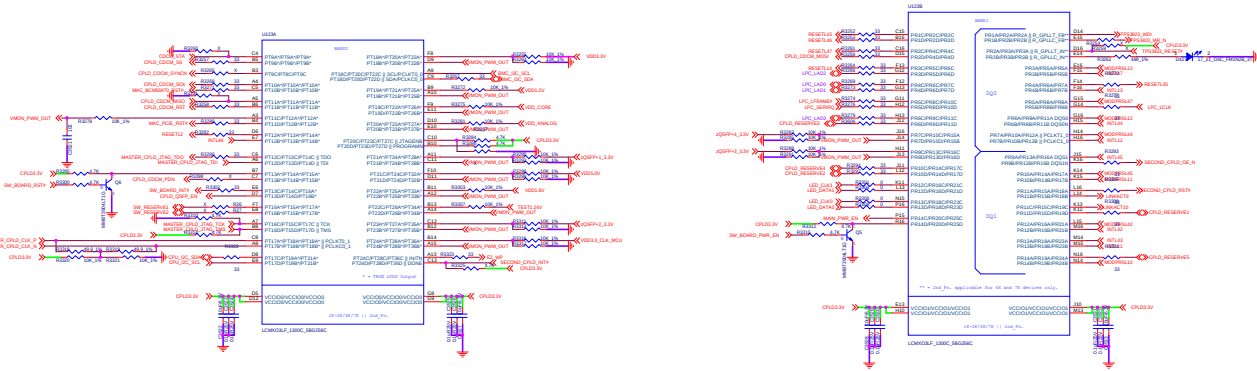
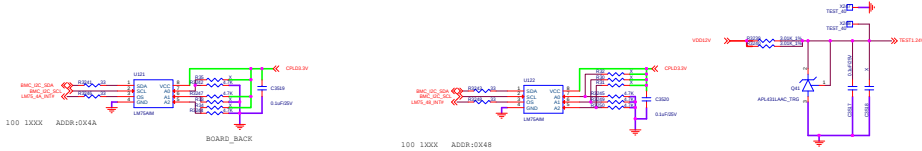


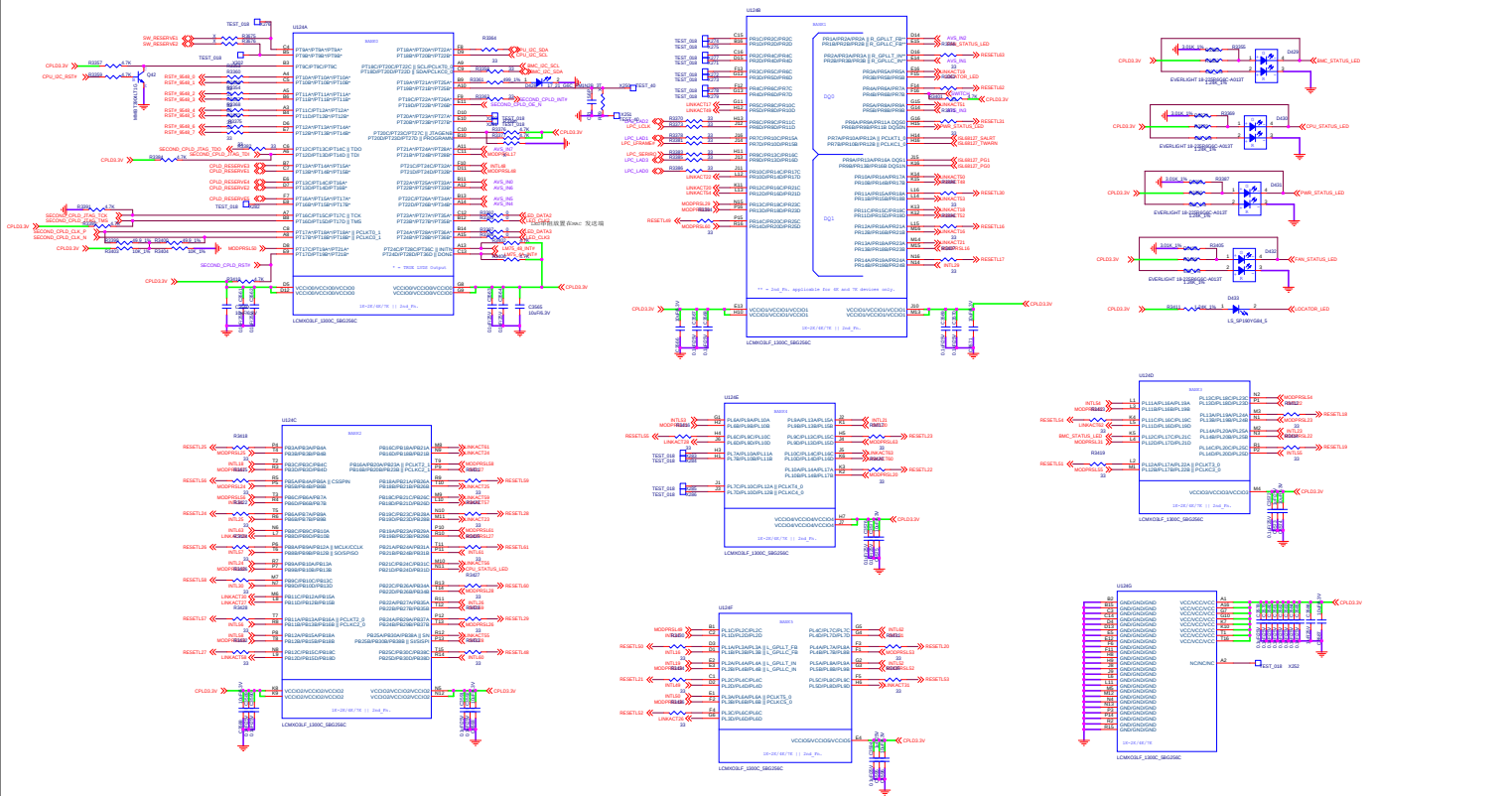


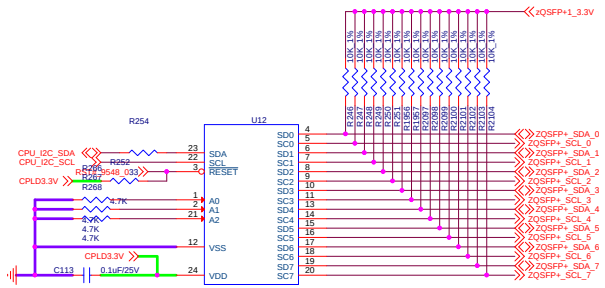






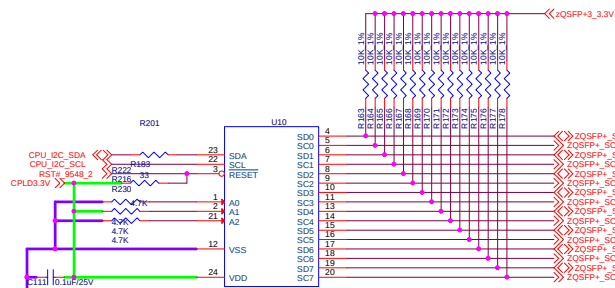






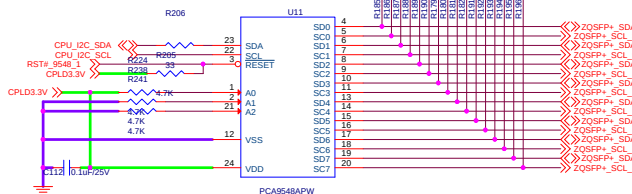
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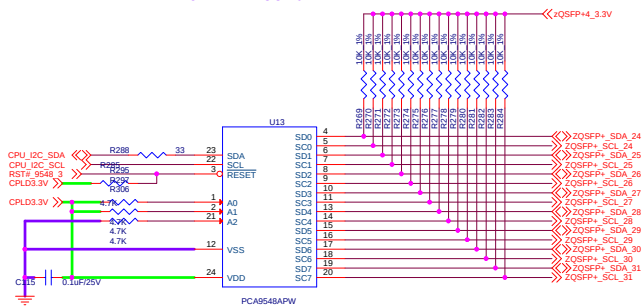
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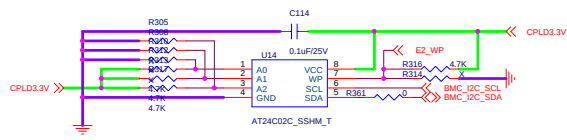
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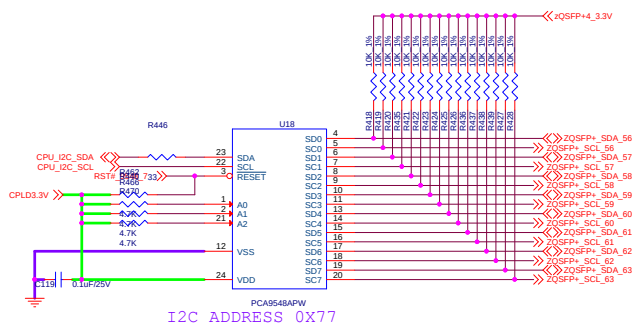
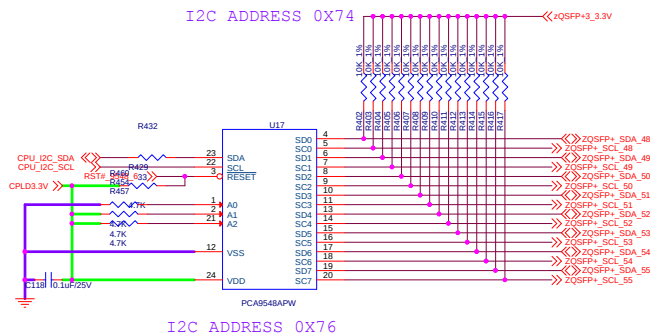
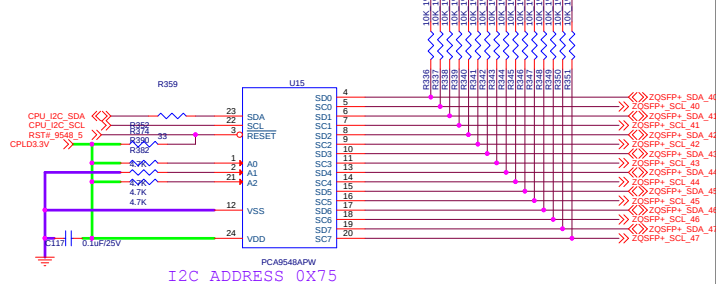
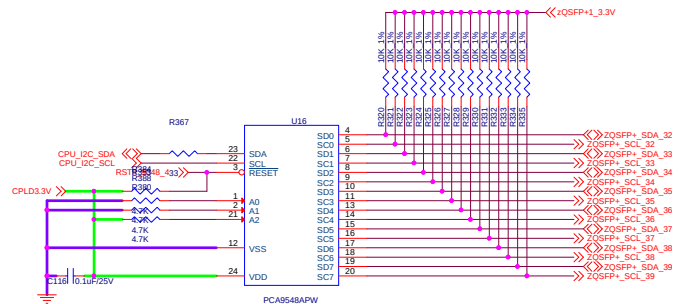


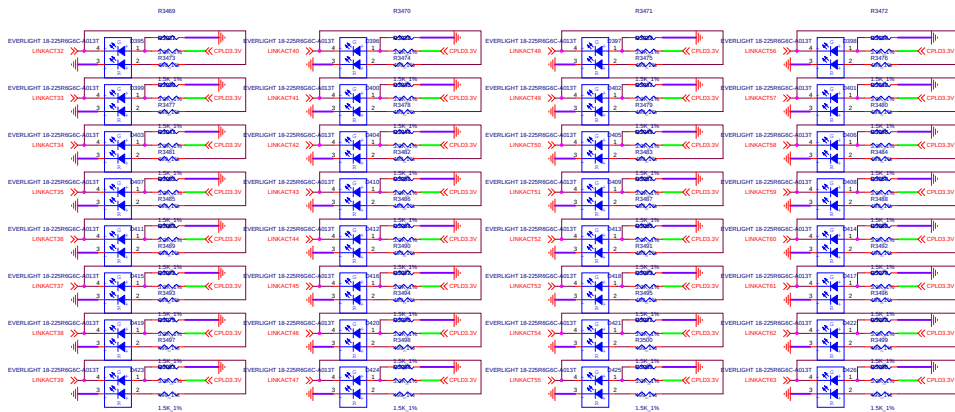
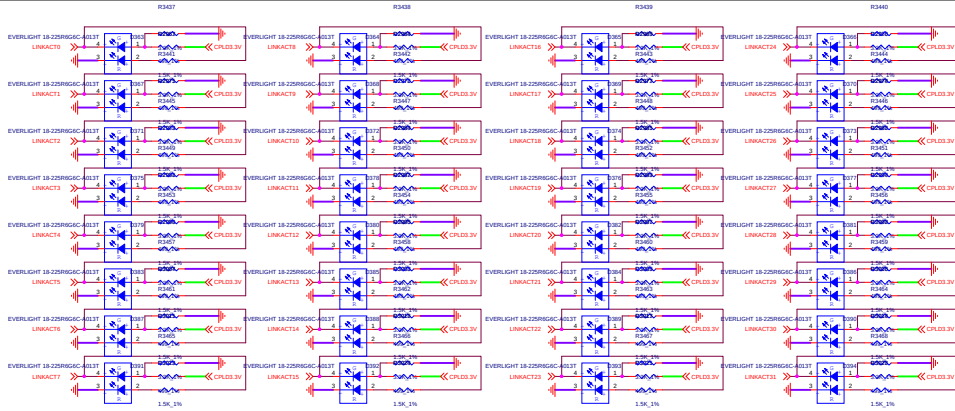
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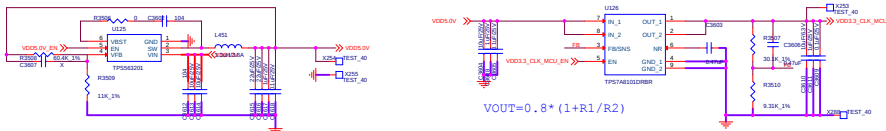
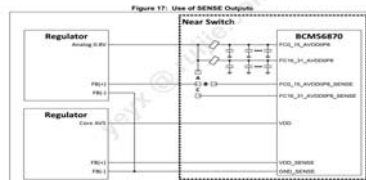
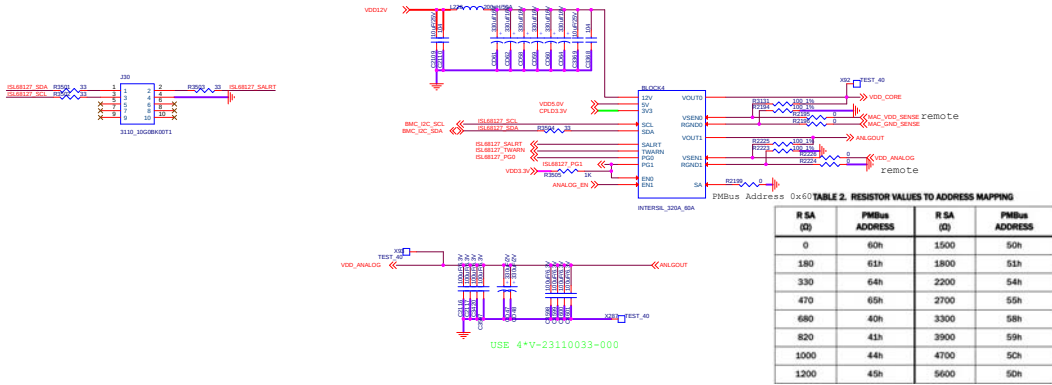
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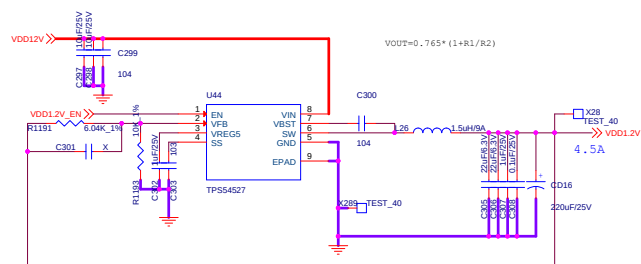
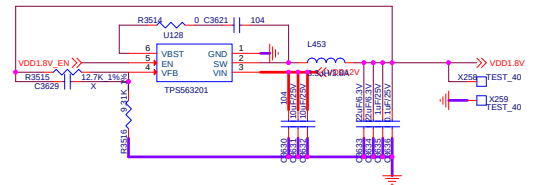


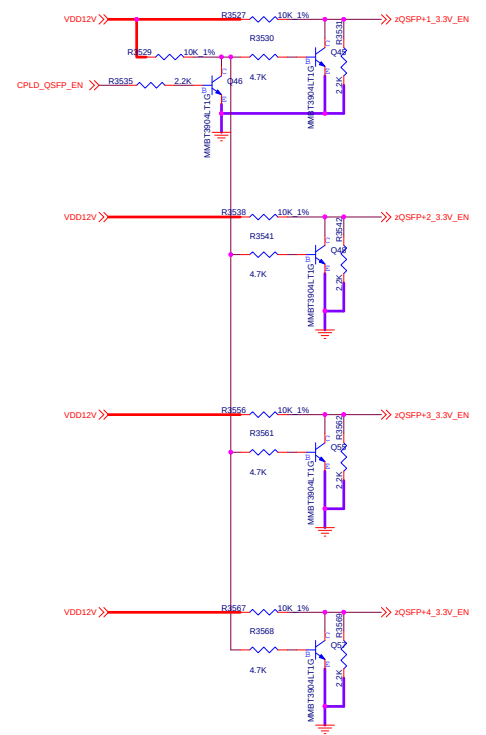
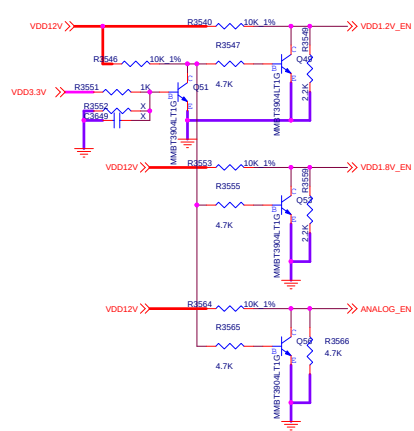
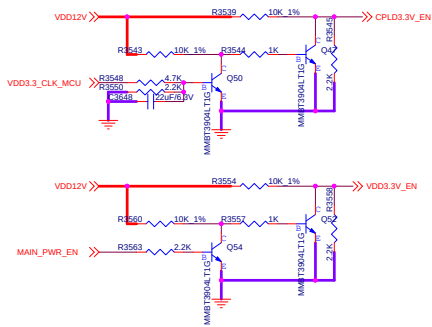
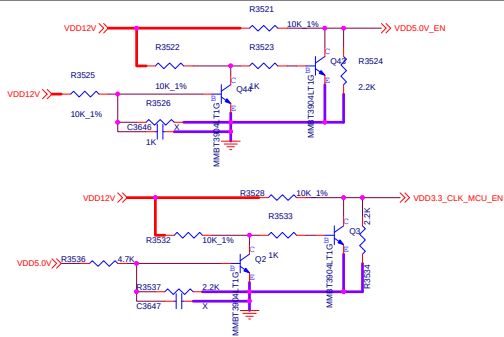
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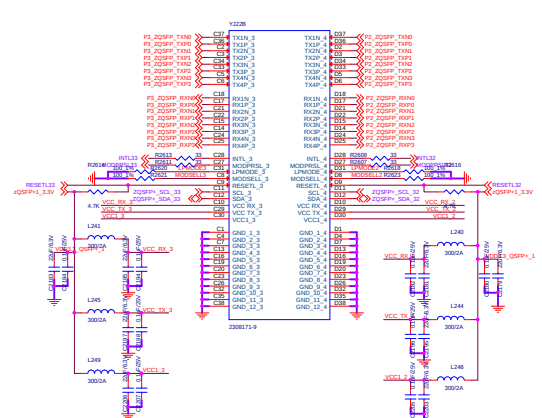
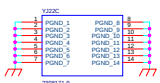
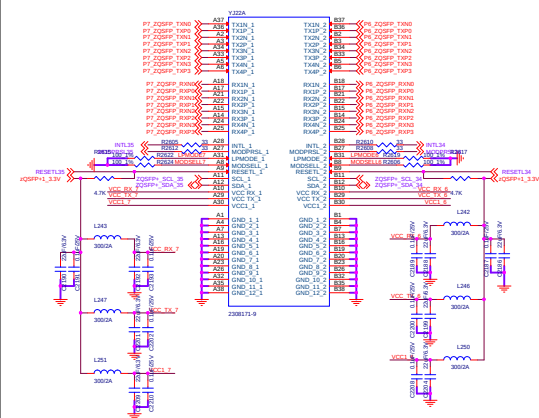
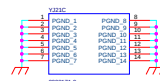


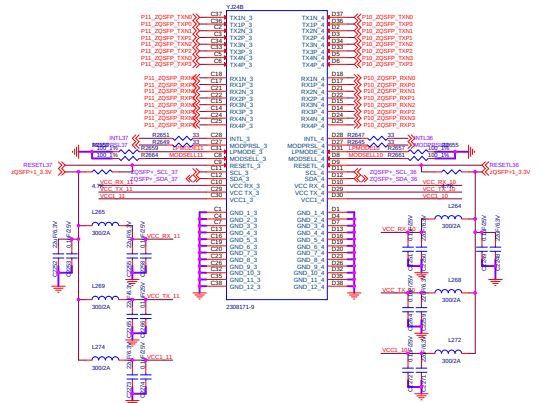
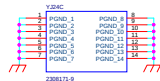
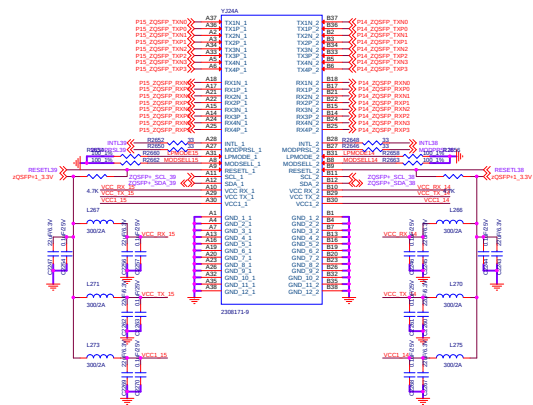
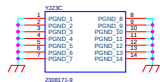


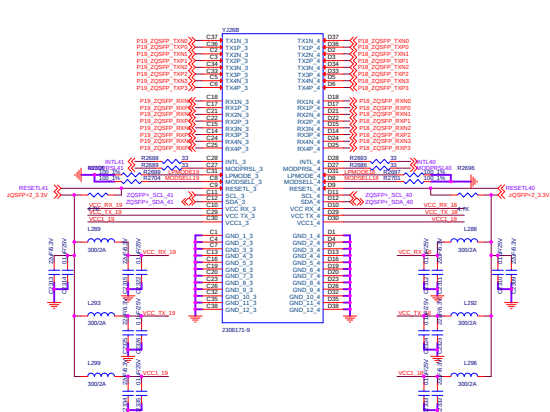
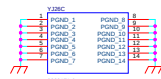
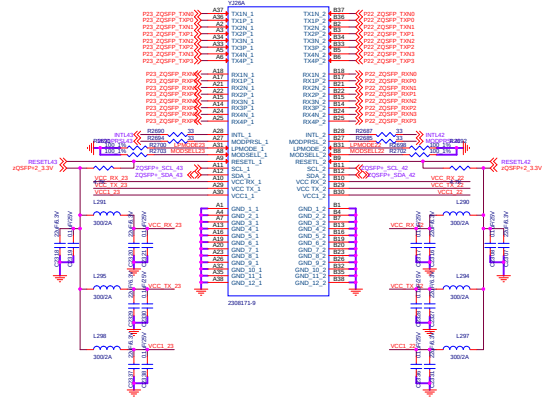
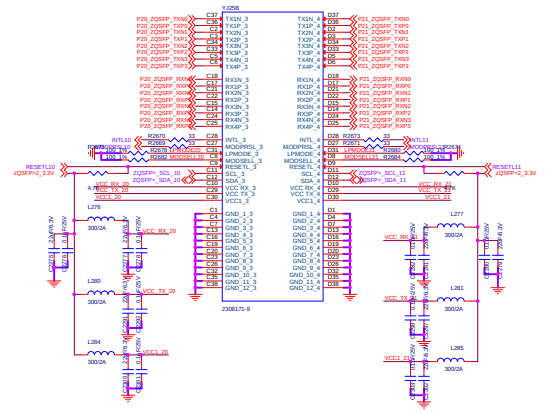
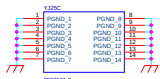
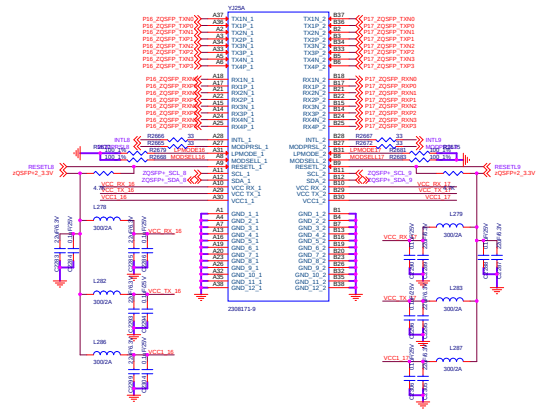


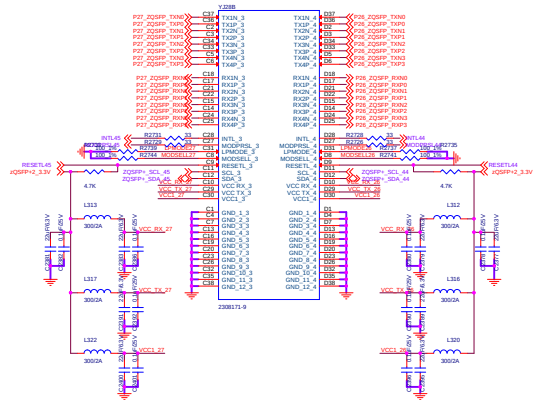
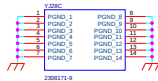
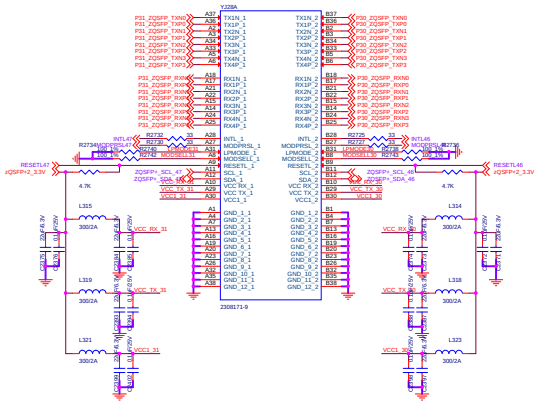
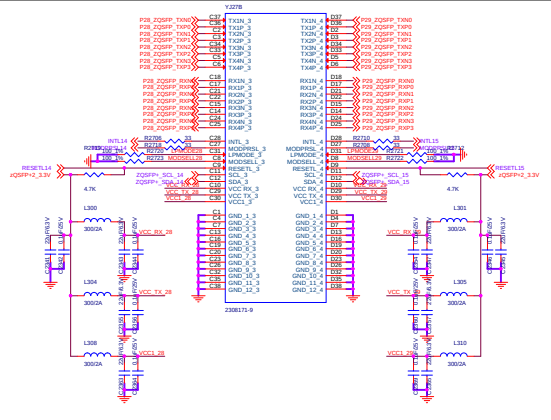
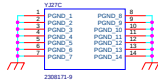
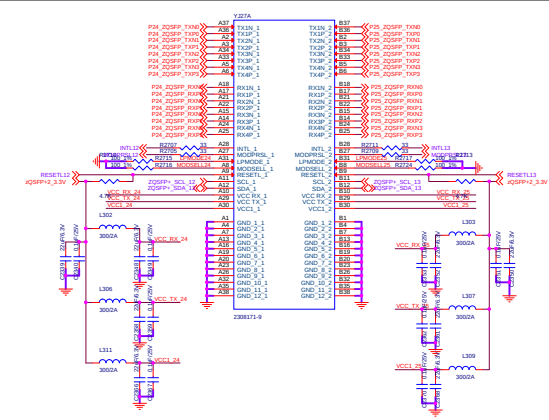


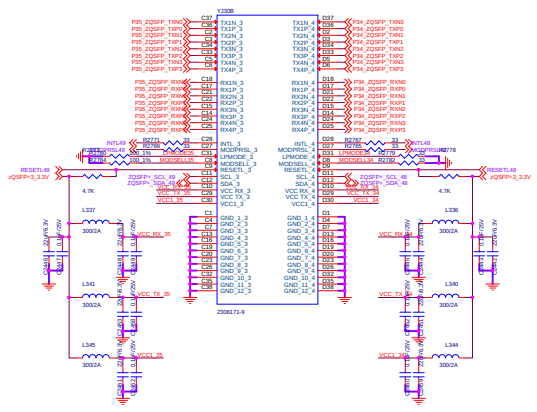
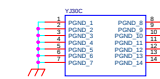
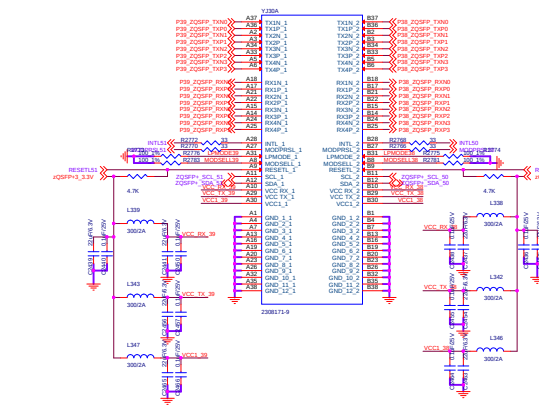
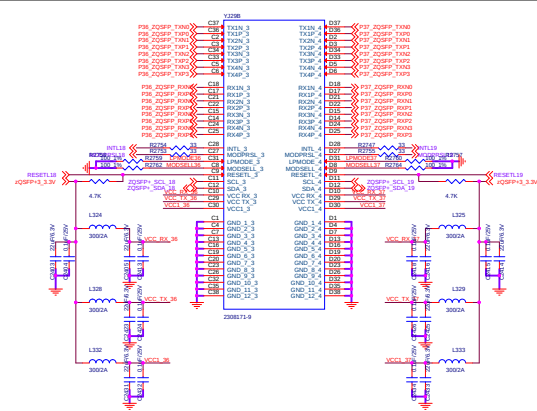
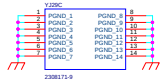
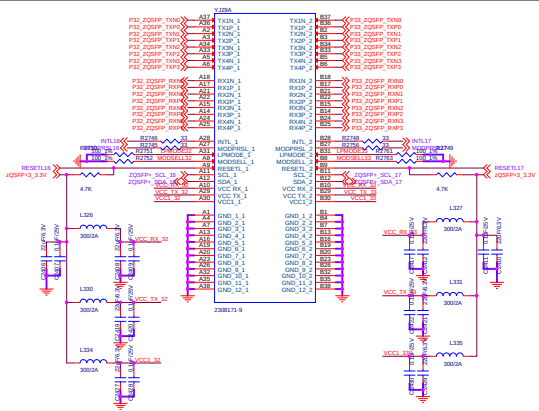


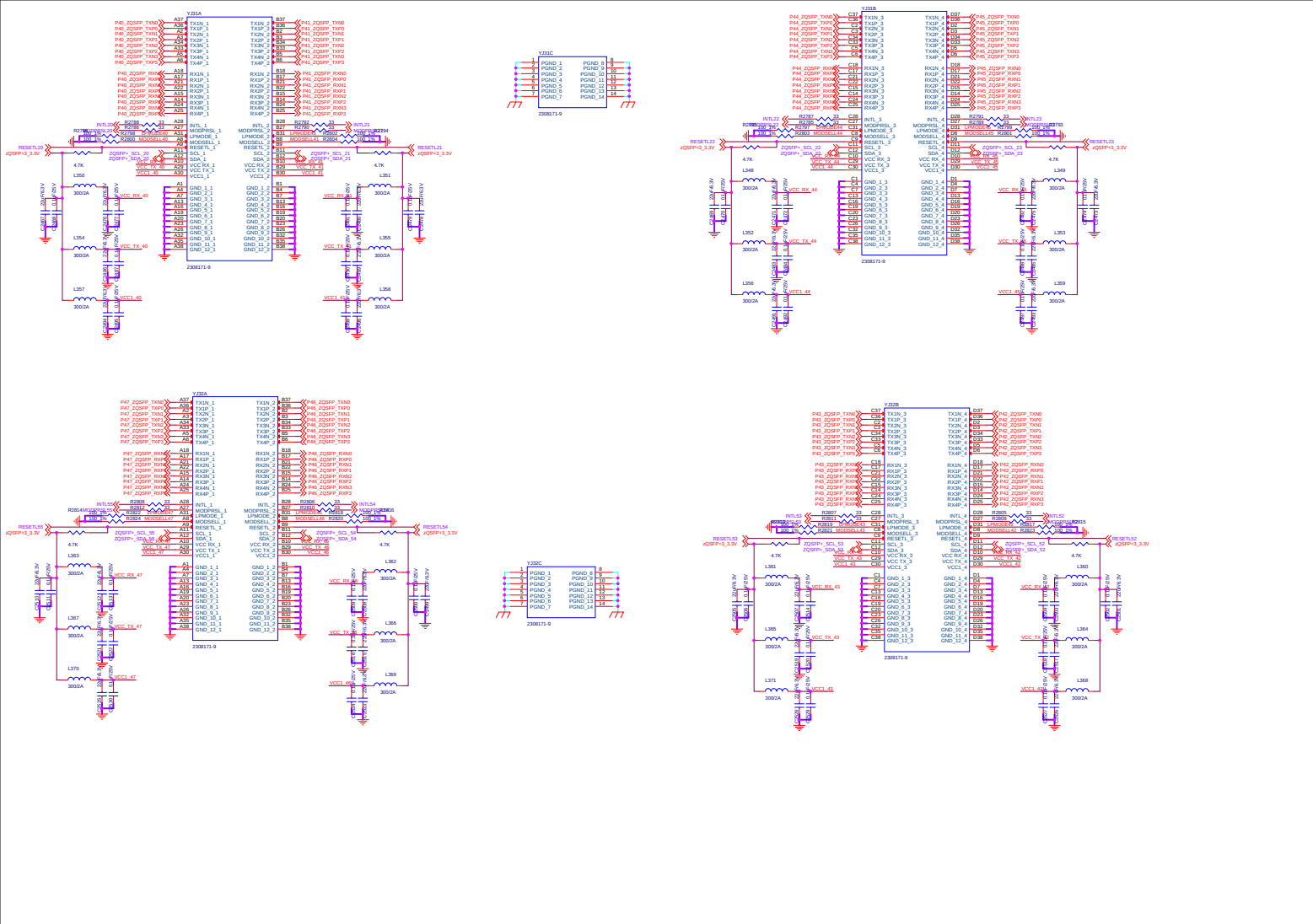


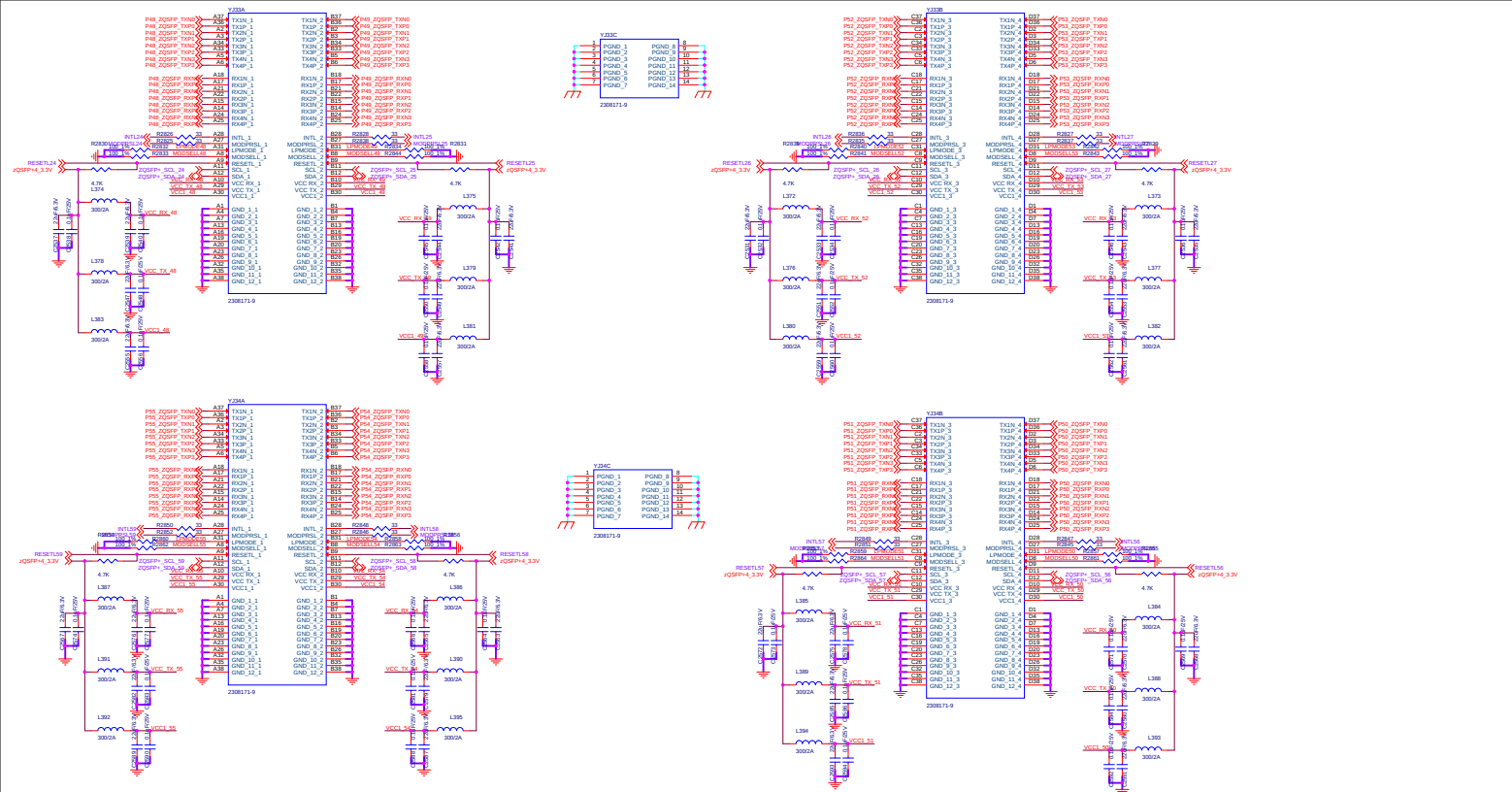


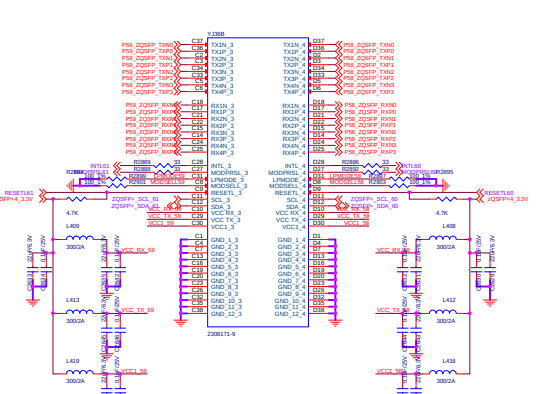
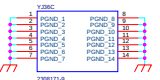
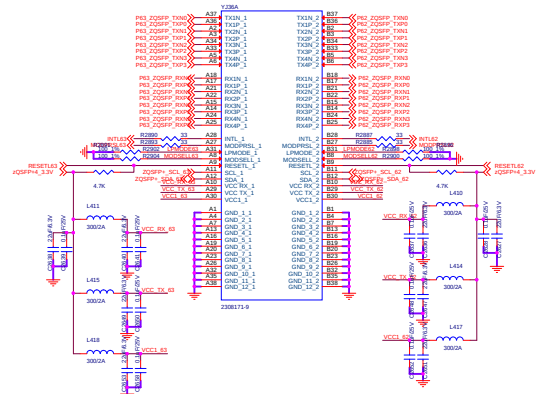
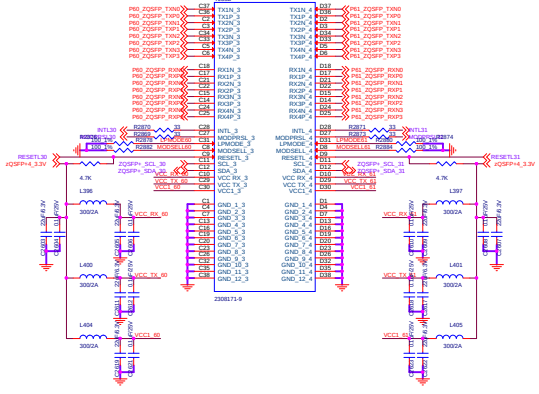
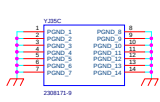
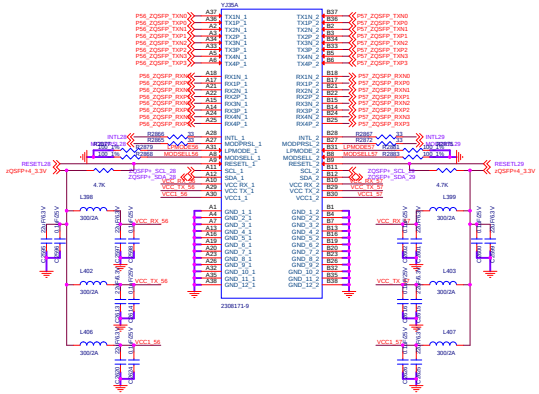


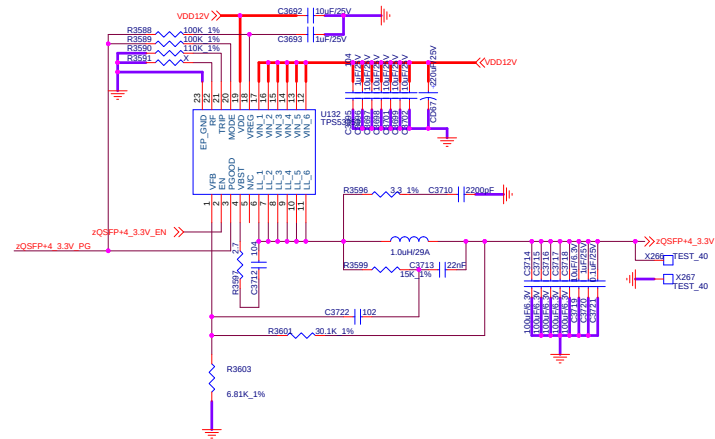
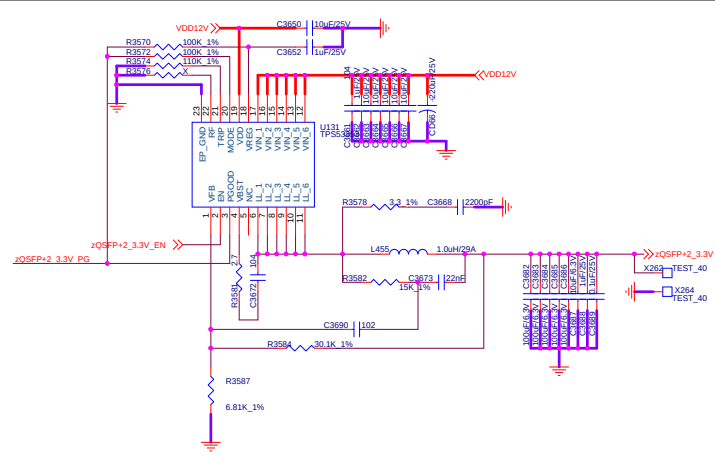


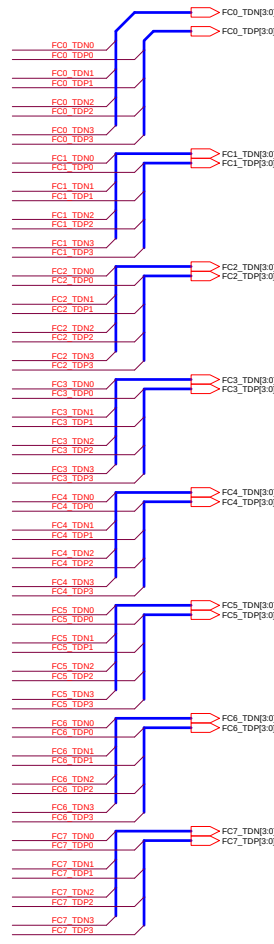
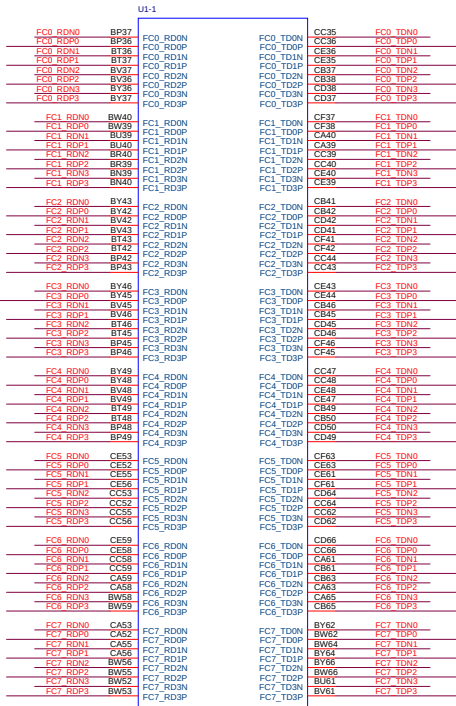
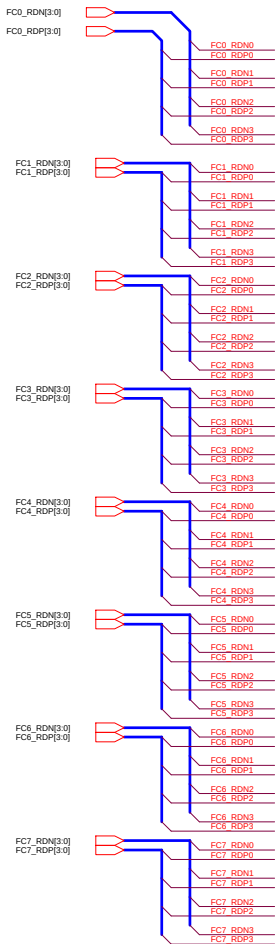


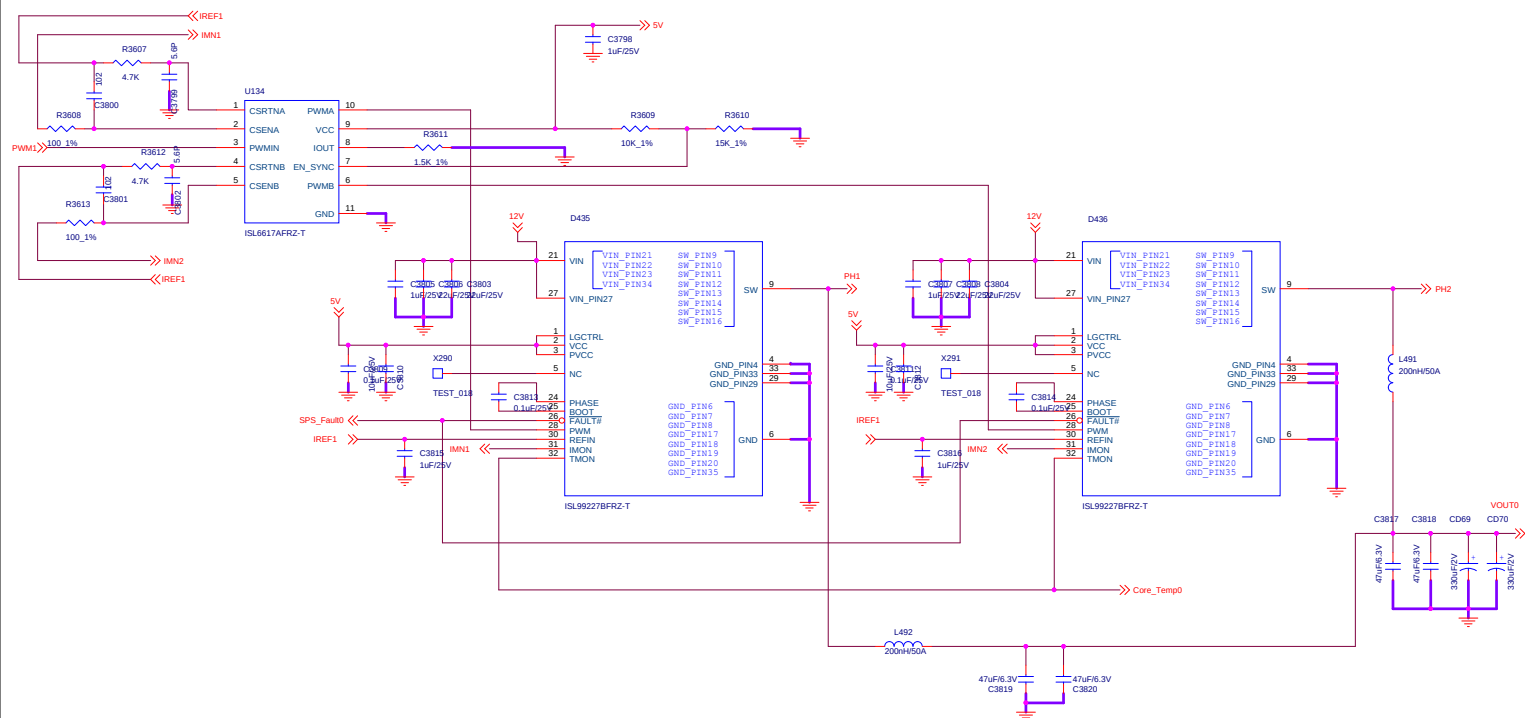


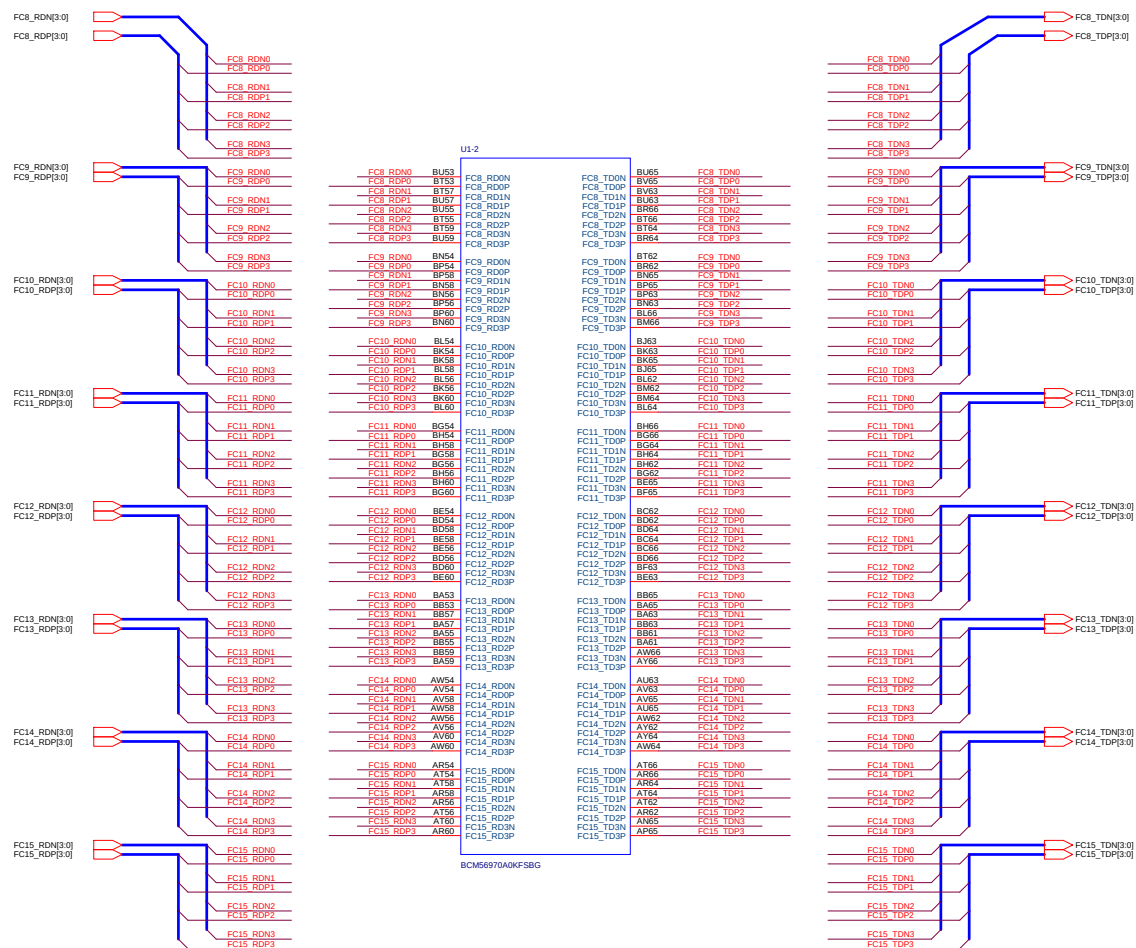


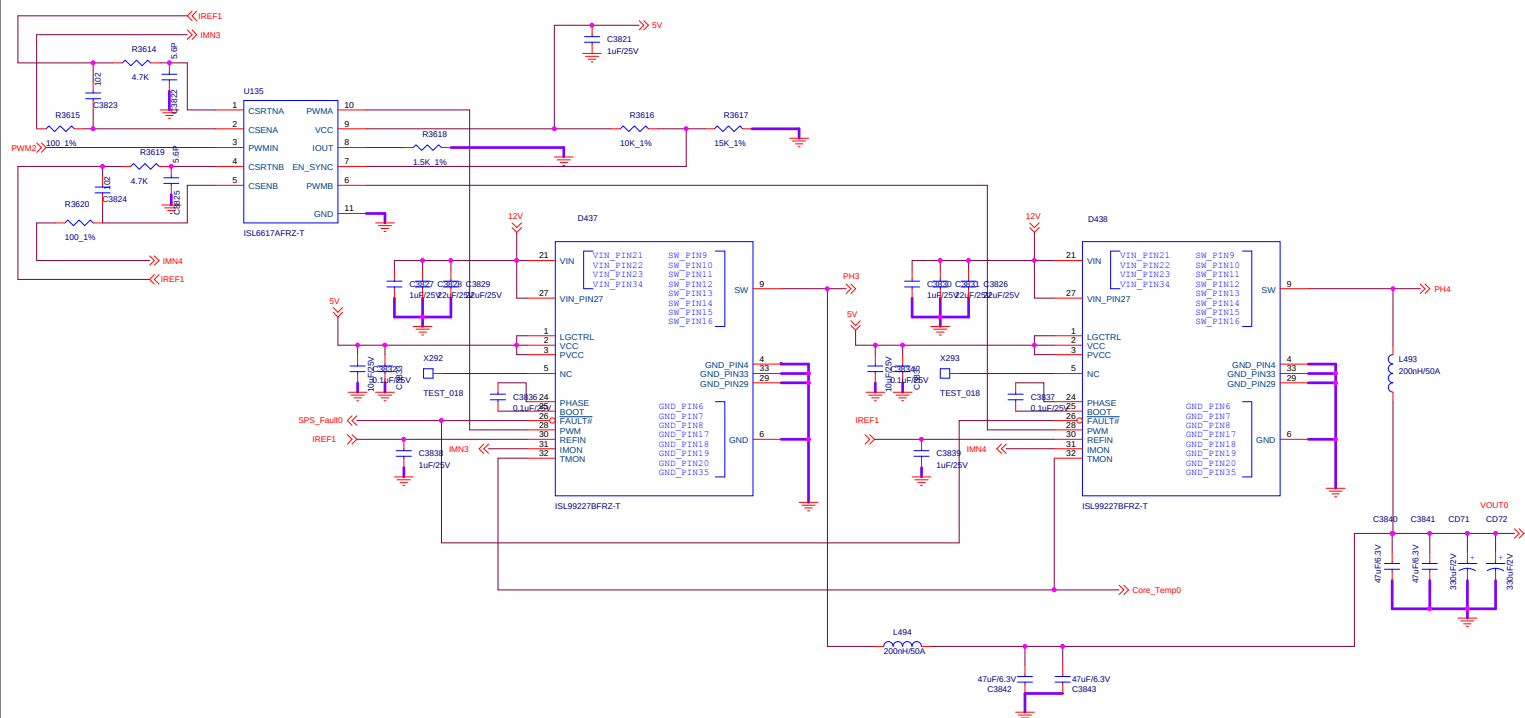


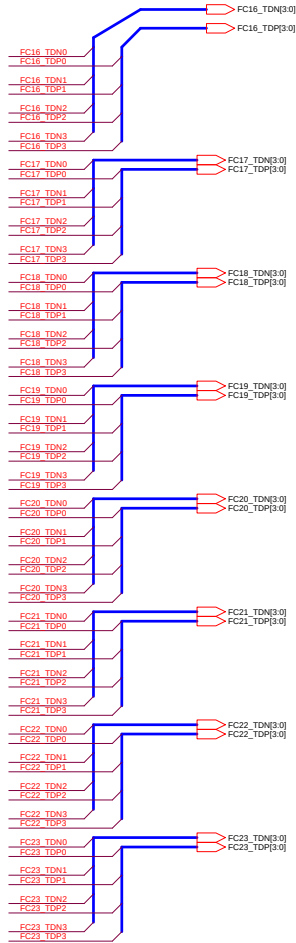
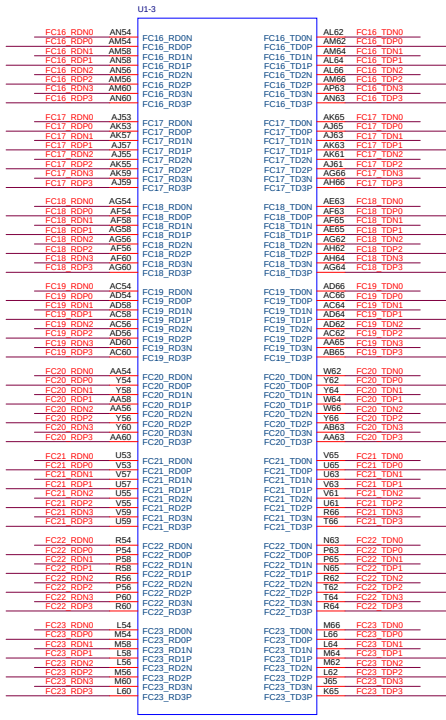
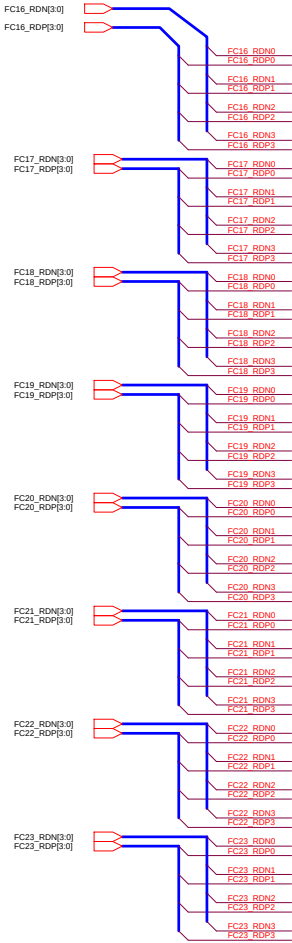


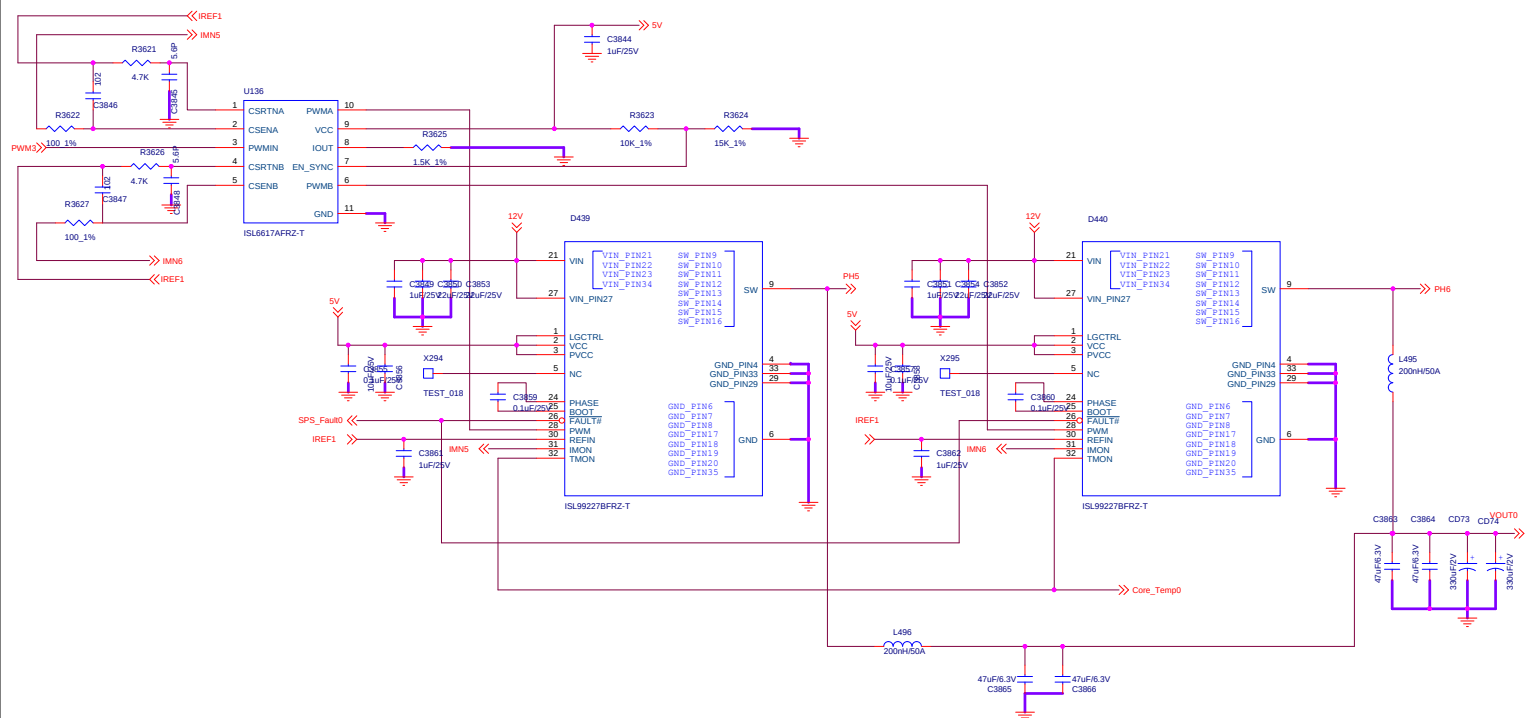


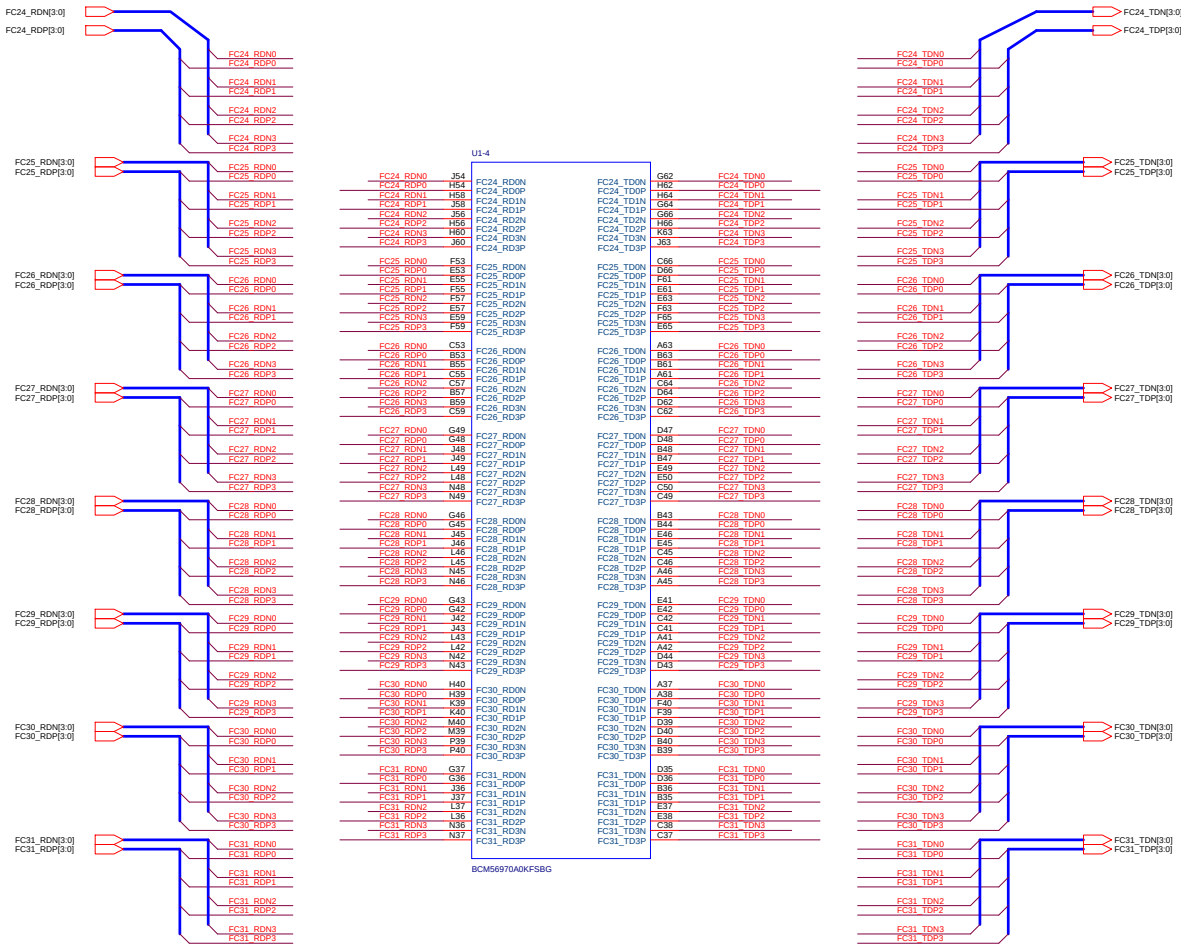


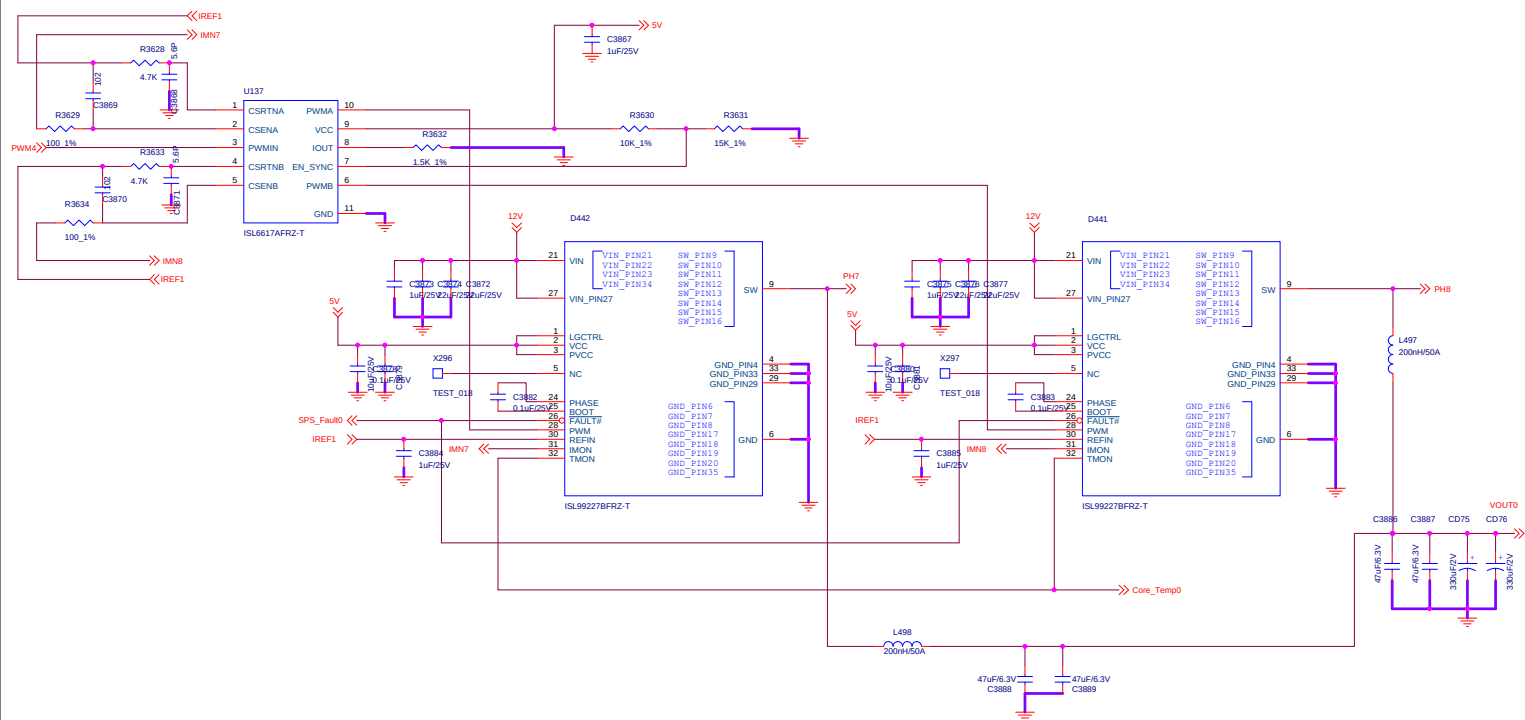


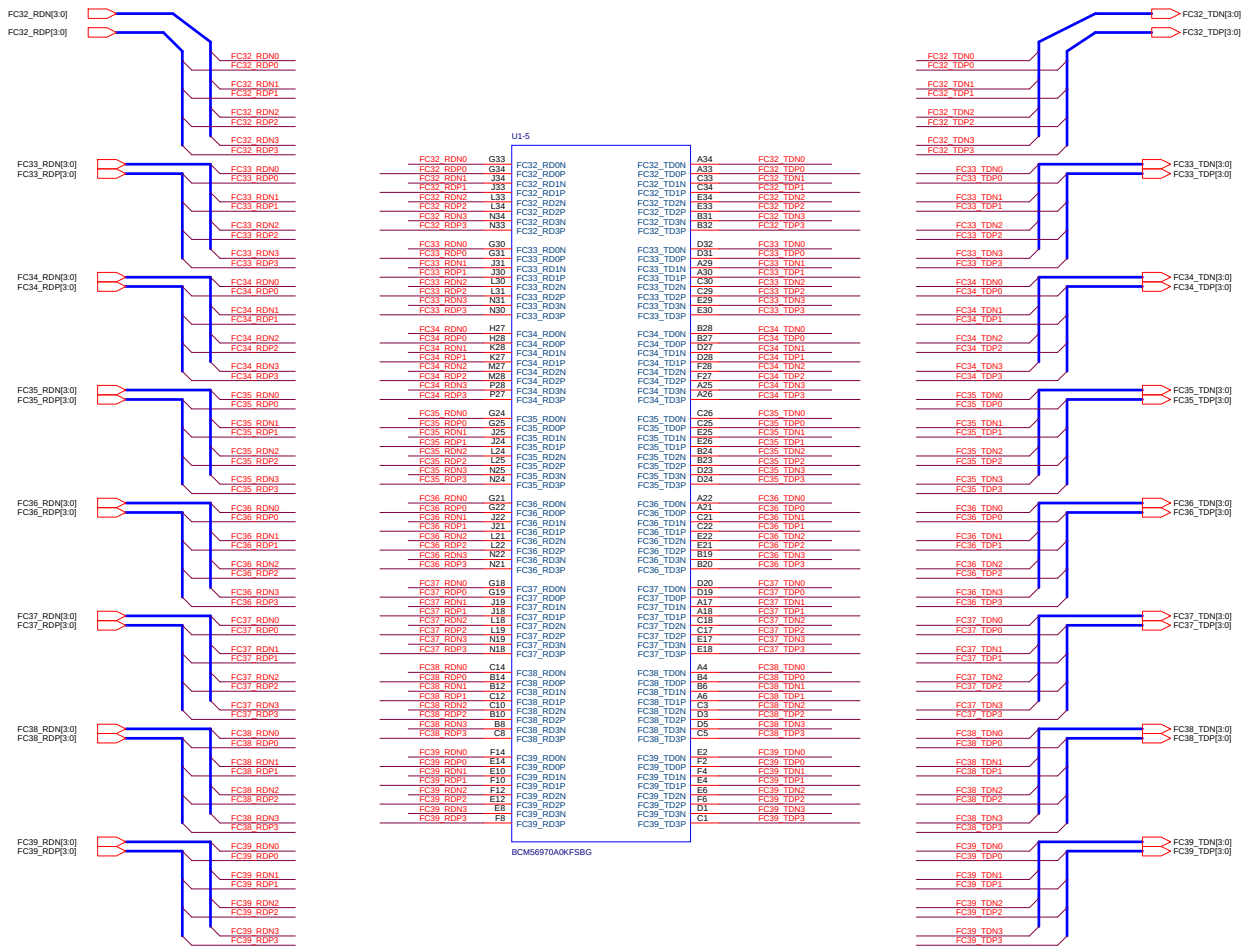


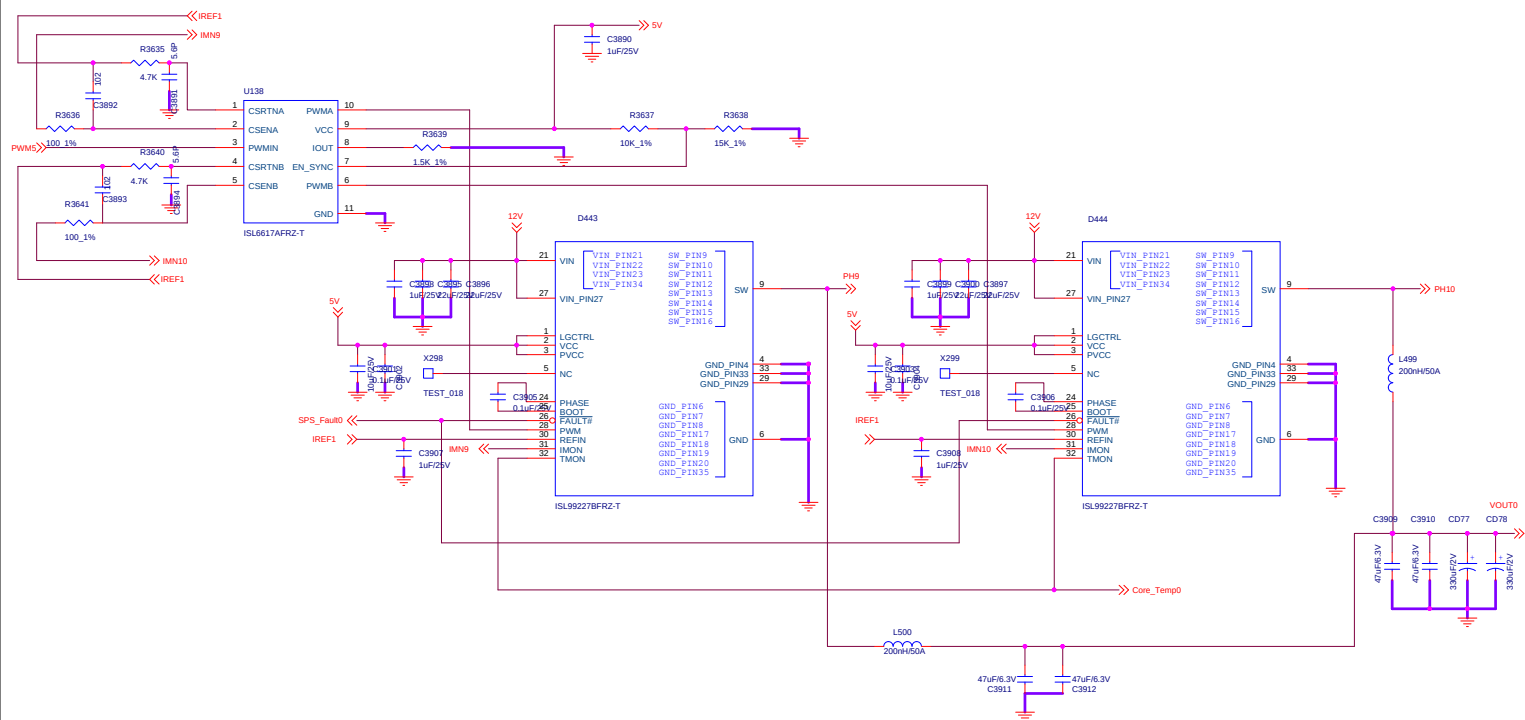


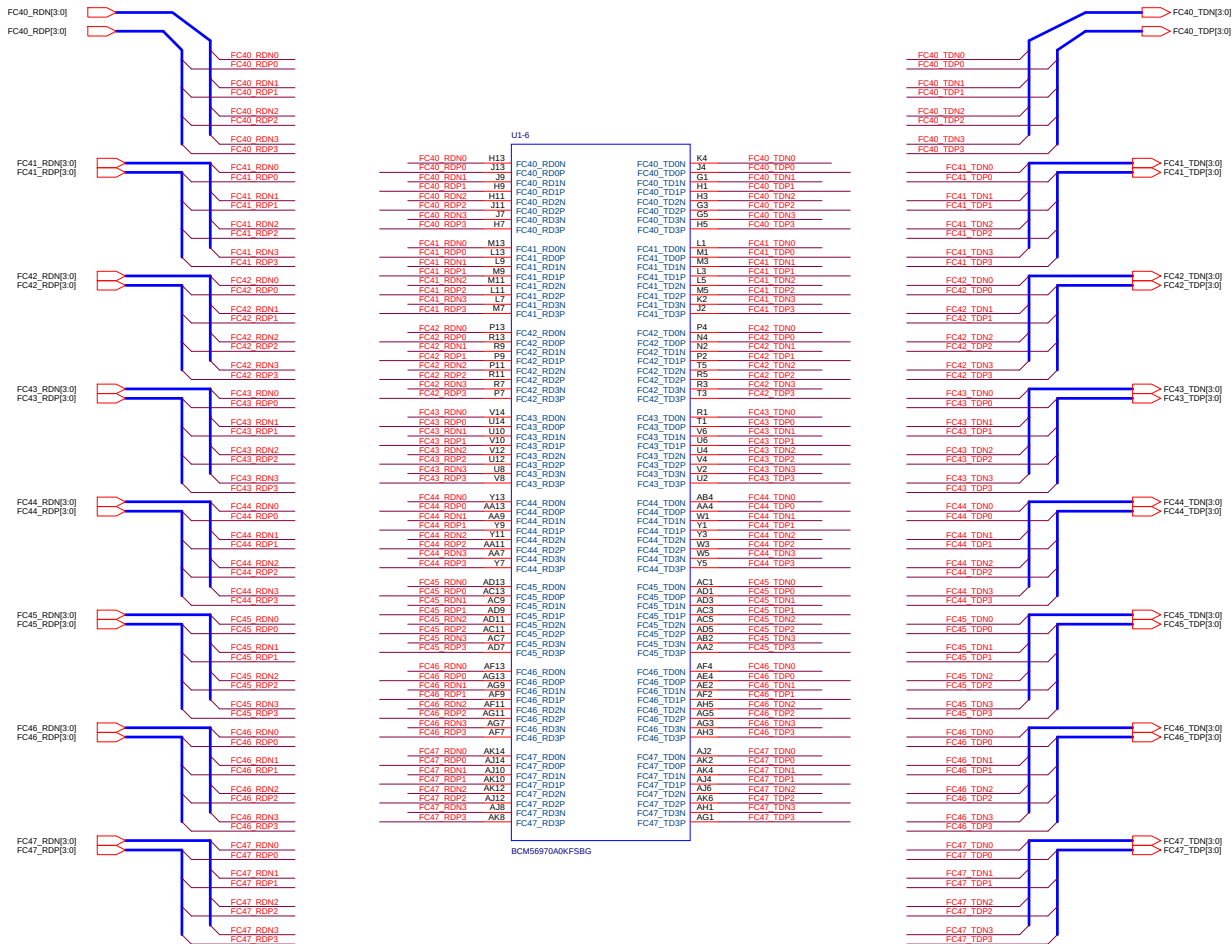


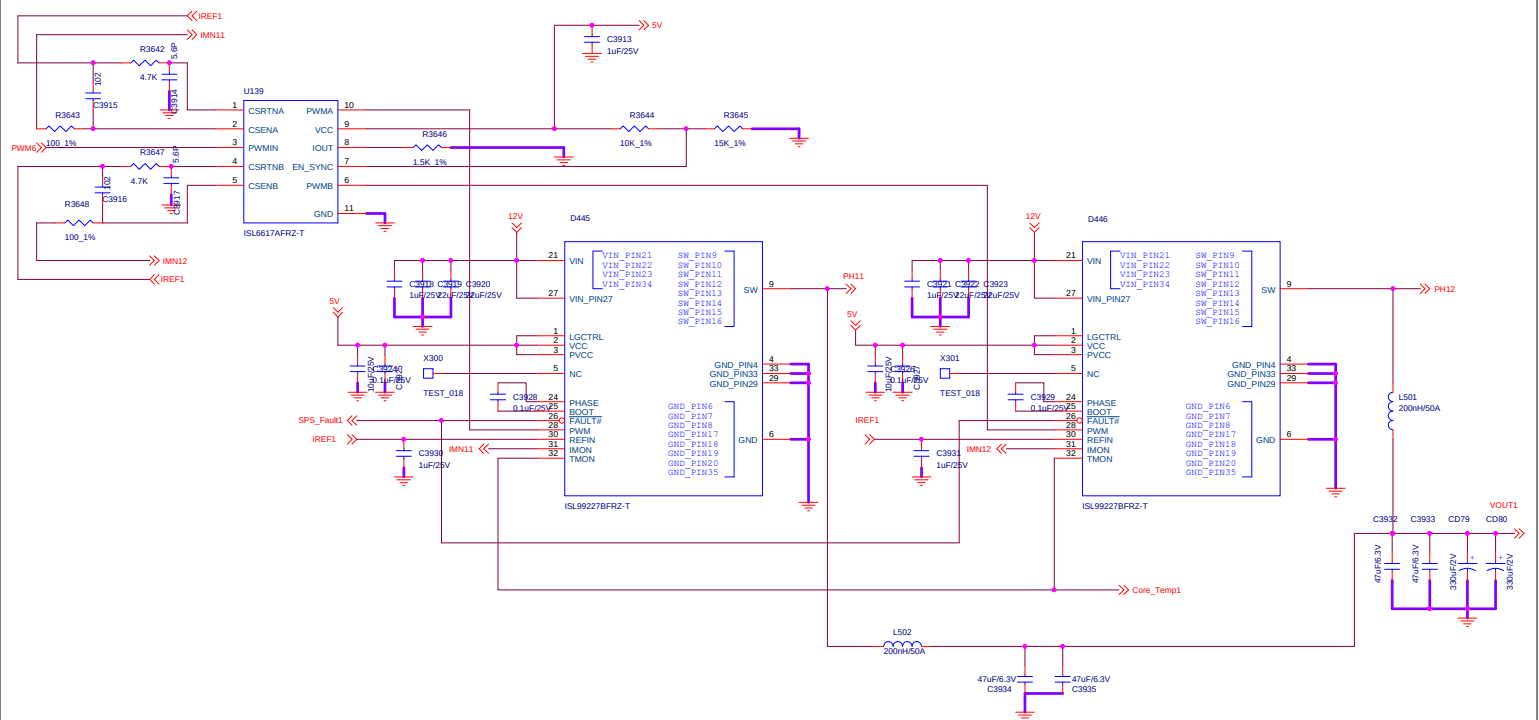


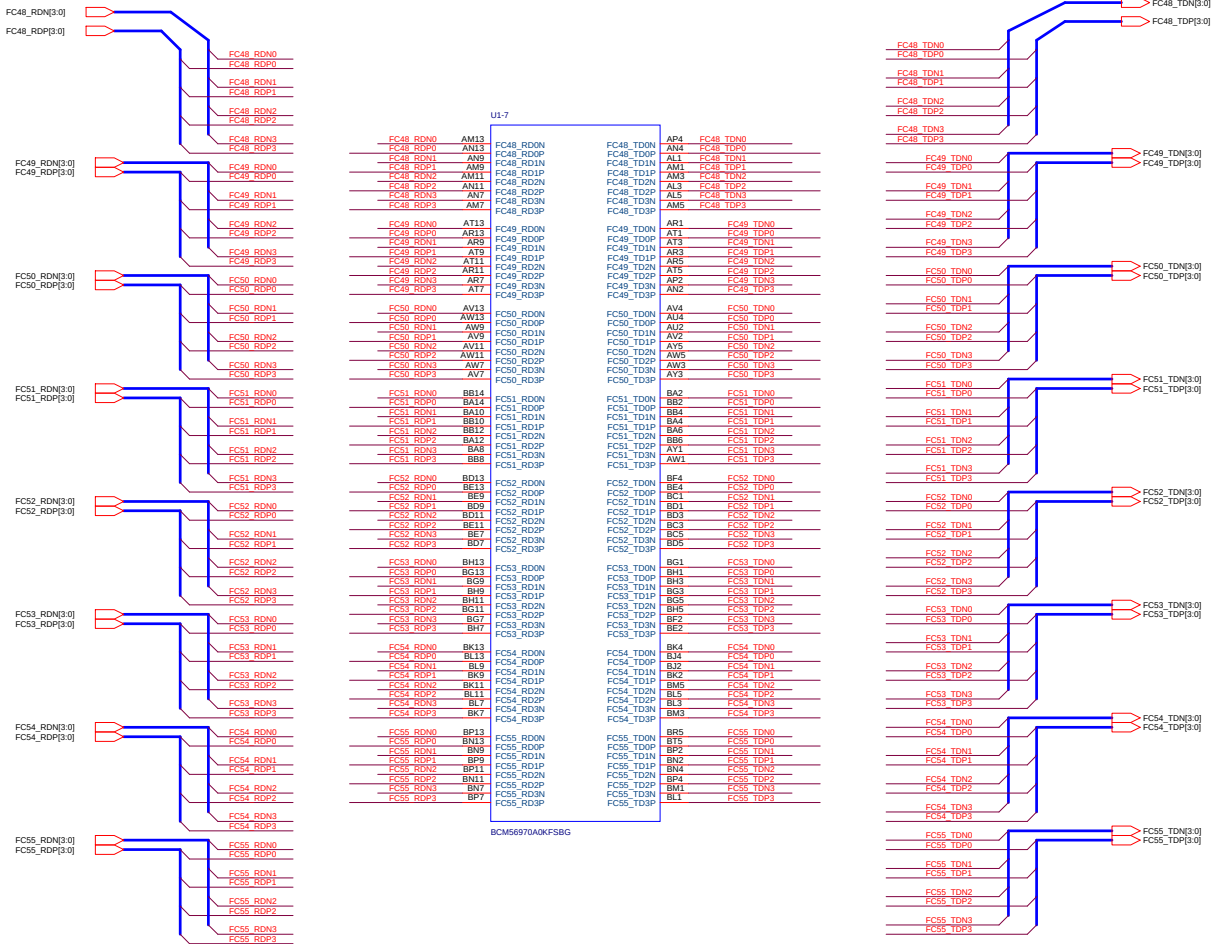


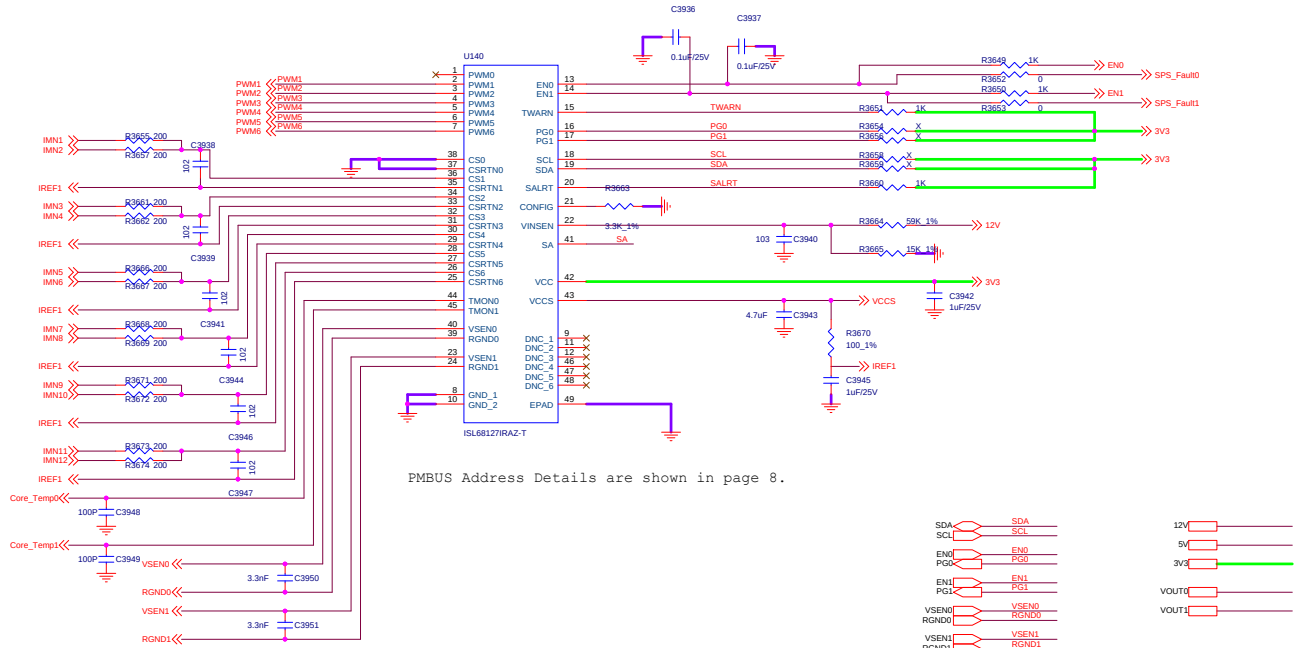












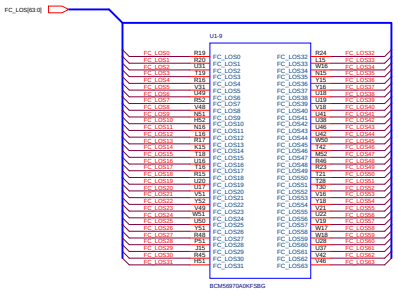
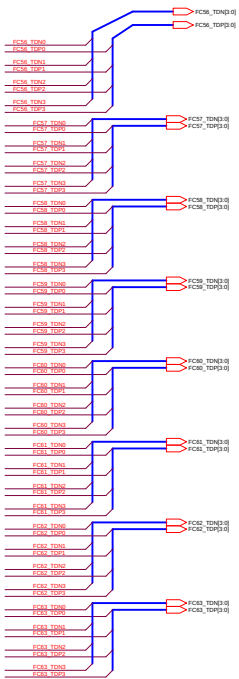
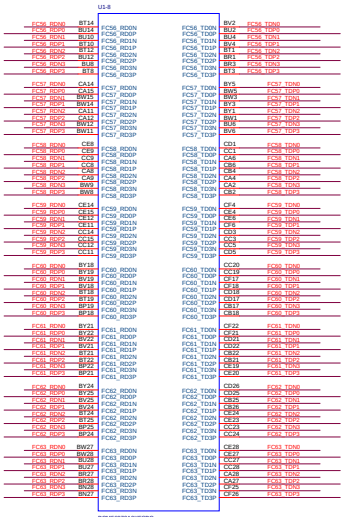
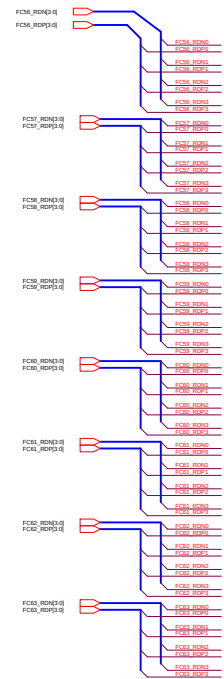
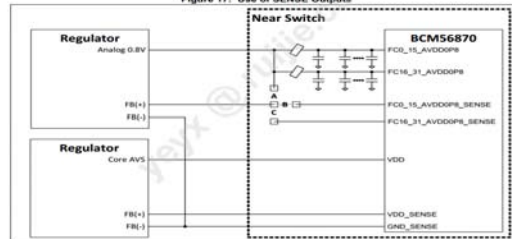
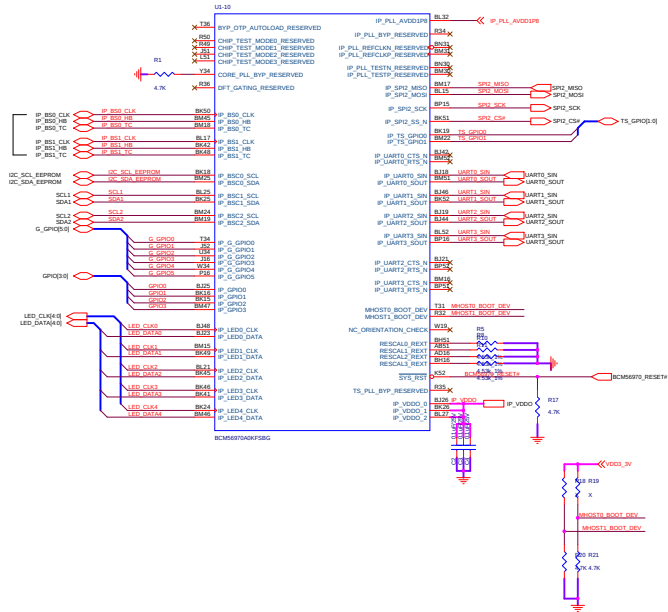
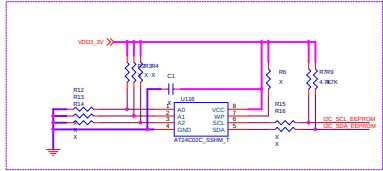


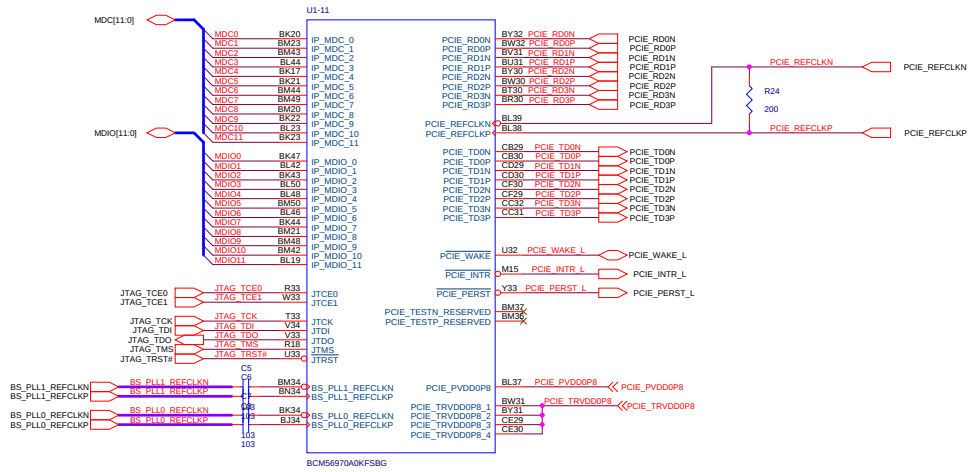
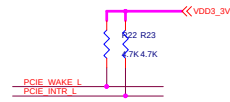
TABLE 2. RESISTOR VALUES TO ADDRESS MAPPING

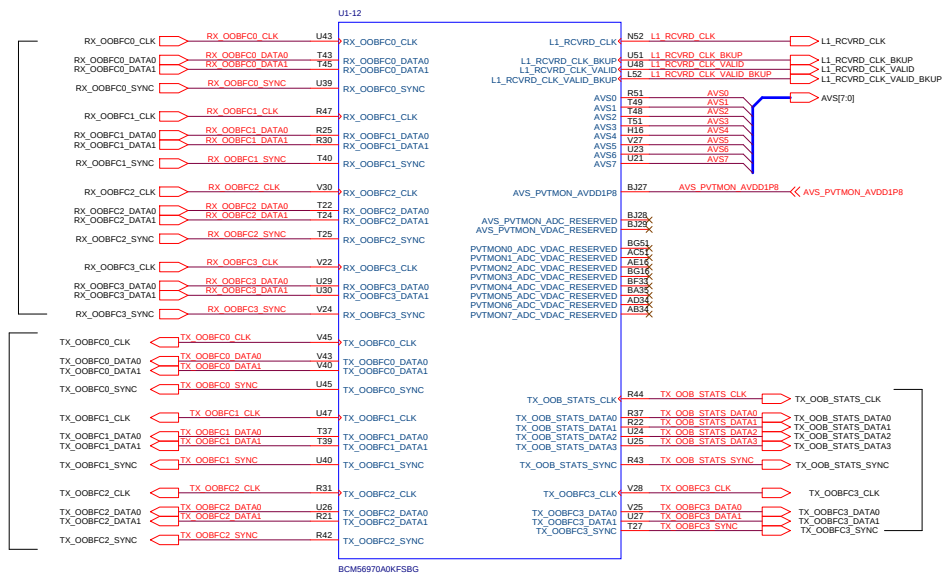
R SA (Ω)	PMBus ADDRESS	R SA (Ω)	PMBus ADDRESS
0	60h	1500	50h
180	61h	1800	51h
330	64h	2200	54h
470	65h	2700	55h
680	40h	3300	58h
820	41h	3900	59h
1000	44h	4700	5Ch
1200	45h	5600	5Dh

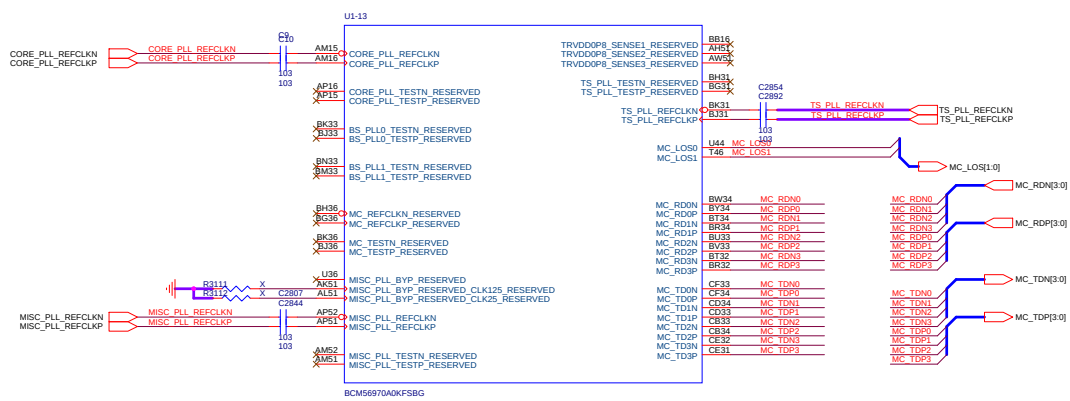
Figure 17: Use of SENSE Outputs

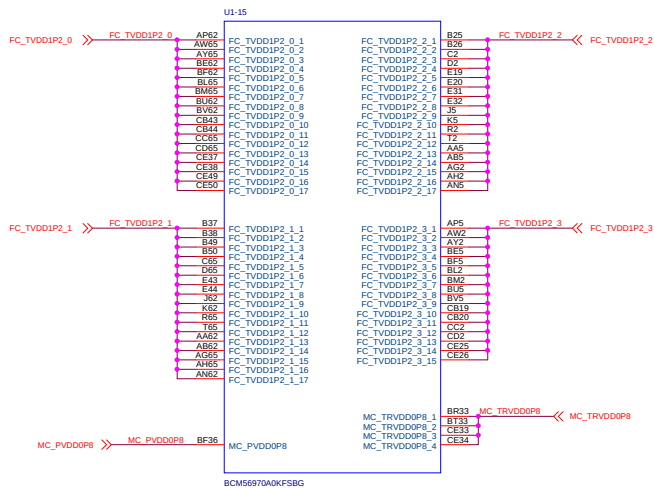


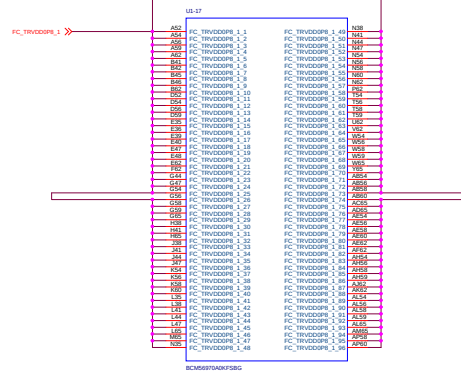
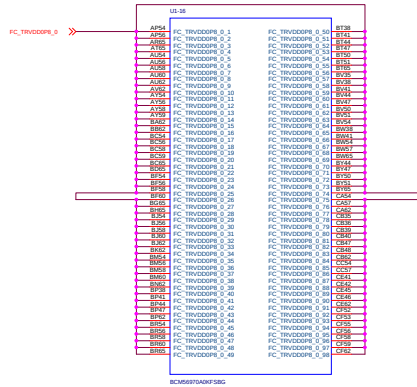


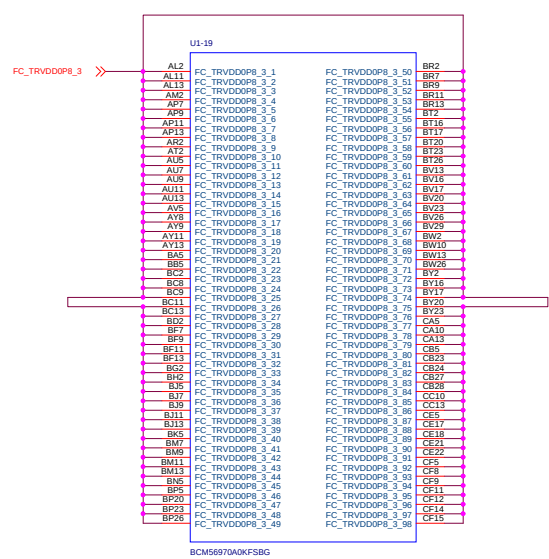
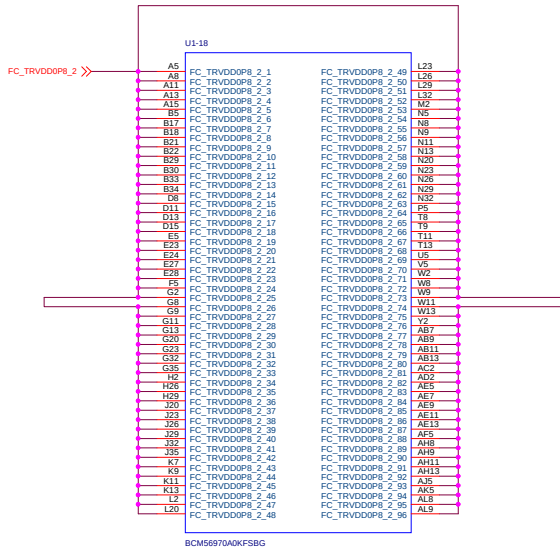


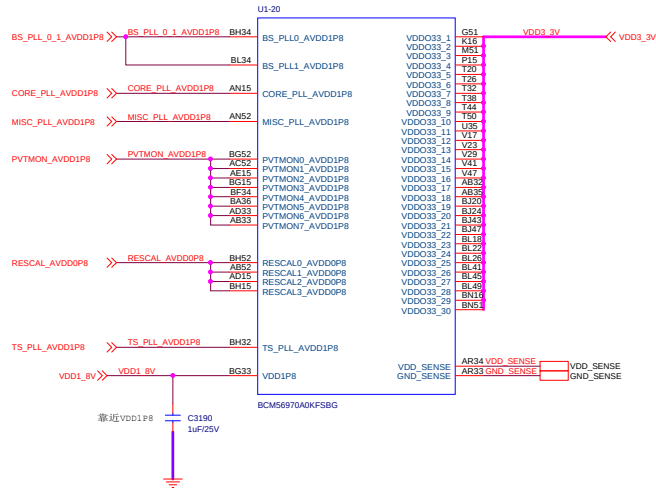












U1-21		
AB29	VDD_1	VDD_81
AB31	VDD_2	VDD_82
AB37	VDD_3	VDD_83
AB39	VDD_4	VDD_84
AB41	VDD_5	VDD_85
AB43	VDD_6	VDD_86
AB45	VDD_7	VDD_87
AB47	VDD_8	VDD_88
AC20	VDD_9	VDD_89
AC22	VDD_10	VDD_90
AC24	VDD_11	VDD_91
AC26	VDD_12	VDD_92
AC28	VDD_13	VDD_93
AC30	VDD_14	VDD_94
AC36	VDD_15	VDD_95
AC38	VDD_16	VDD_96
AC40	VDD_17	VDD_97
AC42	VDD_18	VDD_98
AC44	VDD_19	VDD_99
AC46	VDD_20	VDD_100
AD21	VDD_21	VDD_101
AD23	VDD_22	VDD_102
AD25	VDD_23	VDD_103
AD27	VDD_24	VDD_104
AD29	VDD_25	VDD_105
AD31	VDD_26	VDD_106
AD32	VDD_27	VDD_107
AD35	VDD_28	VDD_108
AD37	VDD_29	VDD_109
AD39	VDD_30	VDD_110
AD41	VDD_31	VDD_111
AD43	VDD_32	VDD_112
AD47	VDD_33	VDD_113
AE20	VDD_34	VDD_114
AE22	VDD_35	VDD_115
AE24	VDD_36	VDD_116
AE26	VDD_37	VDD_117
AE28	VDD_38	VDD_118
AE30	VDD_39	VDD_119
AE32	VDD_40	VDD_120
AE34	VDD_41	VDD_121
AE36	VDD_42	VDD_122
AE38	VDD_43	VDD_123
AE40	VDD_44	VDD_124
AE42	VDD_45	VDD_125
AE44	VDD_46	VDD_126
AE46	VDD_47	VDD_127
AF21	VDD_48	VDD_128
AF23	VDD_49	VDD_129
AF25	VDD_50	VDD_130
AF27	VDD_51	VDD_131
AF29	VDD_52	VDD_132
AF31	VDD_53	VDD_133
AF33	VDD_54	VDD_134
AF35	VDD_55	VDD_135
AF37	VDD_56	VDD_136
AF39	VDD_57	VDD_137
AF41	VDD_58	VDD_138
AF43	VDD_59	VDD_139
AF45	VDD_60	VDD_140
AF47	VDD_61	VDD_141
AG20	VDD_62	VDD_142
AG22	VDD_63	VDD_143
AG24	VDD_64	VDD_144
AG26	VDD_65	VDD_145
AG28	VDD_66	VDD_146
AG30	VDD_67	VDD_147
AG32	VDD_68	VDD_148
AG34	VDD_69	VDD_149
AG36	VDD_70	VDD_150
AG38	VDD_71	VDD_151
AG40	VDD_72	VDD_152
AG42	VDD_73	VDD_153
AG44	VDD_74	VDD_154
AG46	VDD_75	VDD_155
AH21	VDD_76	VDD_156
AH23	VDD_77	VDD_157
AH25	VDD_78	VDD_158
AH27	VDD_79	VDD_159
	VDD_80	VDD_160

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VDD_CORE

U1-22		
AP21	VDD_161	AW21
AP23	VDD_162	AW23
AP25	VDD_163	AW25
AP27	VDD_164	AW27
AP29	VDD_165	AW29
AP31	VDD_166	AW31
AP33	VDD_167	AW33
AP35	VDD_168	AW35
AP37	VDD_169	AW37
AP39	VDD_170	AW39
AP41	VDD_171	AW41
AP43	VDD_172	AW43
AP45	VDD_173	AW45
AP47	VDD_174	AW47
AR21	VDD_175	AV21
AR23	VDD_176	AV23
AR25	VDD_177	AV25
AR27	VDD_178	AV27
AR29	VDD_179	AV29
AR31	VDD_180	AV31
AR33	VDD_181	AV33
AR35	VDD_182	AV35
AR37	VDD_183	AV37
AR39	VDD_184	AV39
AR41	VDD_185	AV41
AR43	VDD_186	AV43
AR45	VDD_187	AV45
AR47	VDD_188	AV47
AT21	VDD_189	AV21
AT23	VDD_190	AV23
AT25	VDD_191	AV25
AT27	VDD_192	AV27
AT29	VDD_193	AV29
AT31	VDD_194	AV31
AT33	VDD_195	AV33
AT35	VDD_196	AV35
AT37	VDD_197	AV37
AT39	VDD_198	AV39
AT41	VDD_199	AV41
AT43	VDD_200	AV43
AT45	VDD_201	AV45
AV21	VDD_202	AV21
AV23	VDD_203	AV23
AV25	VDD_204	AV25
AV27	VDD_205	AV27
AV29	VDD_206	AV29
AV31	VDD_207	AV31
AV33	VDD_208	AV33
AV35	VDD_209	AV35
AV37	VDD_210	AV37
AV39	VDD_211	AV39
AV41	VDD_212	AV41
AV43	VDD_213	AV43
AV45	VDD_214	AV45
AV47	VDD_215	AV47
AV49	VDD_216	AV49
AV51	VDD_217	AV51
AV53	VDD_218	AV53
AV55	VDD_219	AV55
AV57	VDD_220	AV57
AV59	VDD_221	AV59
AV61	VDD_222	AV61
AV63	VDD_223	AV63
AV65	VDD_224	AV65
AV67	VDD_225	AV67
AV69	VDD_226	AV69
AV71	VDD_227	AV71
AV73	VDD_228	AV73
AV75	VDD_229	AV75
AV77	VDD_230	AV77
AV79	VDD_231	AV79
AV81	VDD_232	AV81
AV83	VDD_233	AV83
AV85	VDD_234	AV85
AV87	VDD_235	AV87
AV89	VDD_236	AV89
AV91	VDD_237	AV91
AV93	VDD_238	AV93
AV95	VDD_239	AV95
AV97	VDD_240	AV97

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